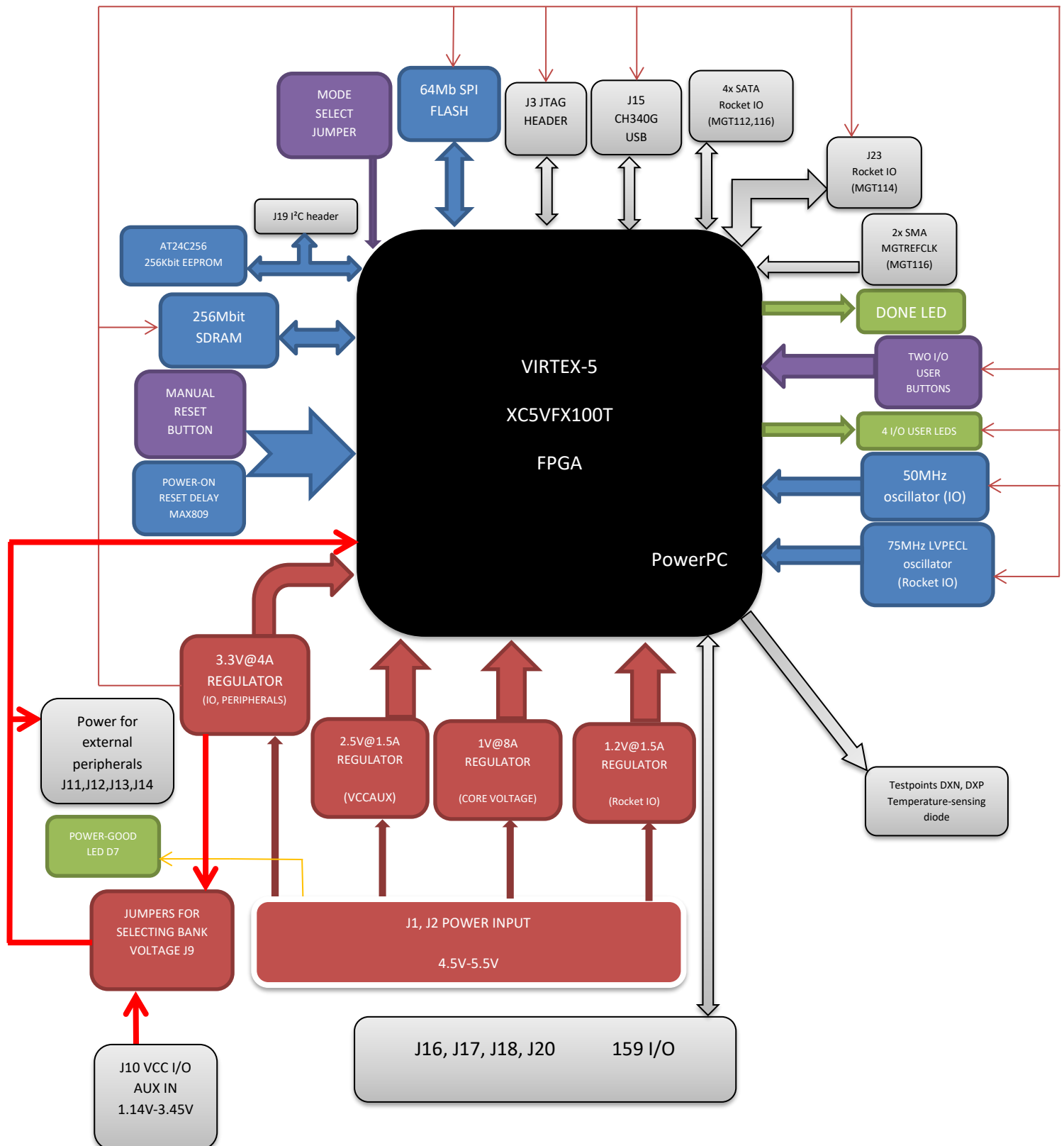
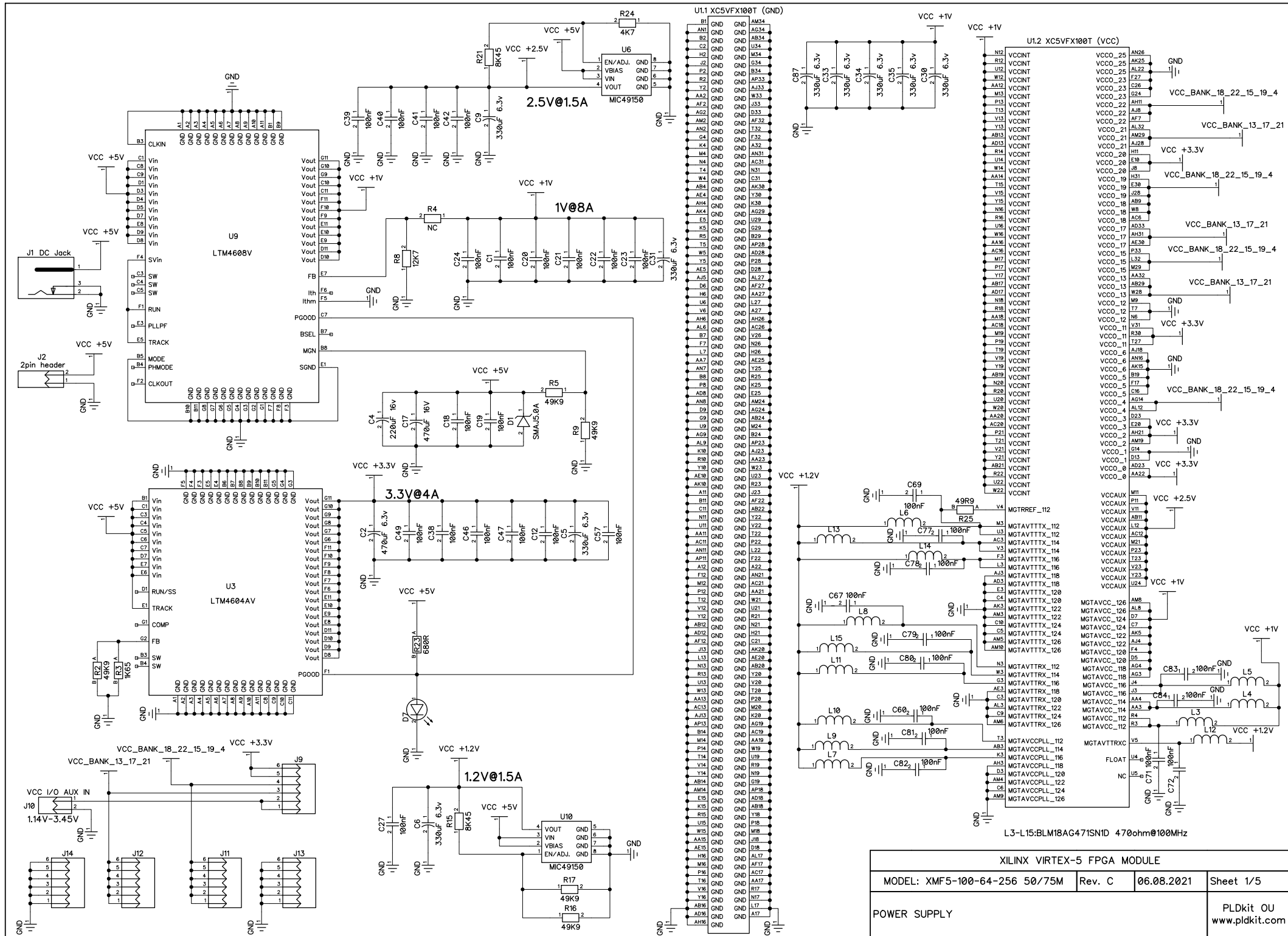
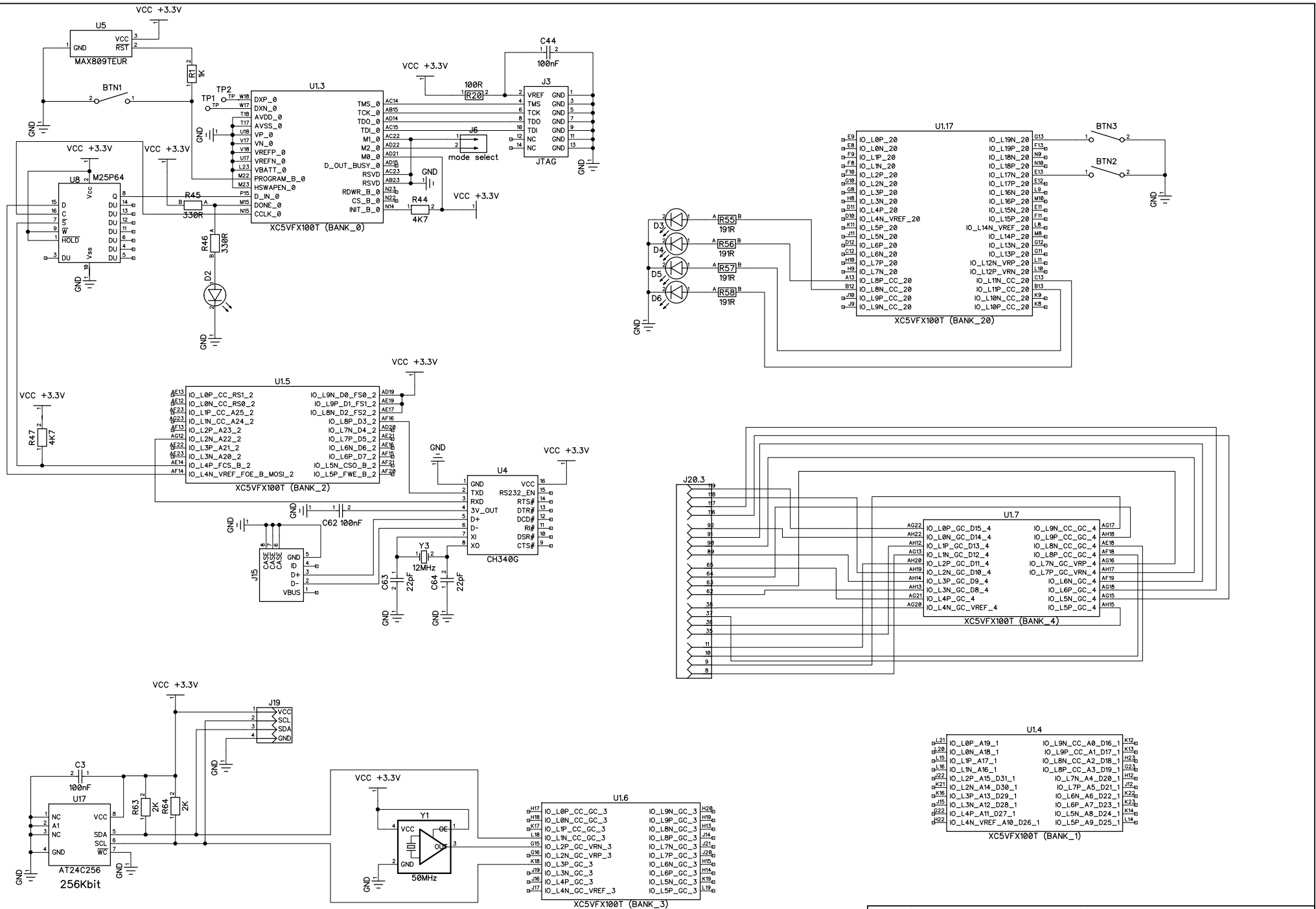


XILINX VIRTEX-5 FPGA MODULE BLOCK SCHEME

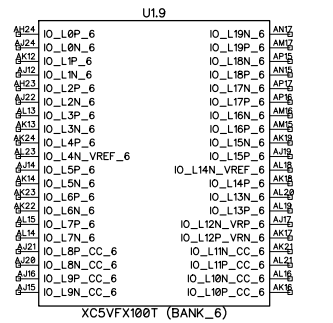
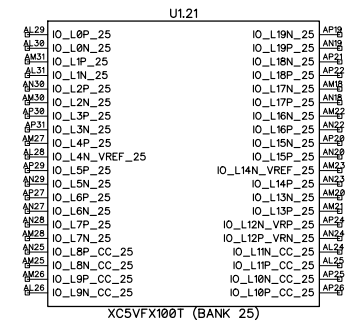
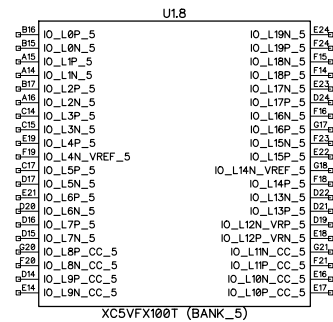
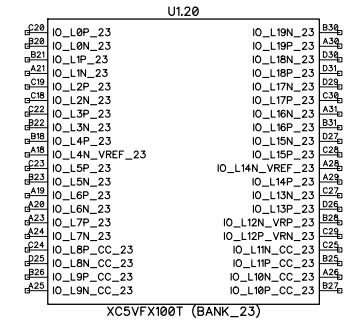
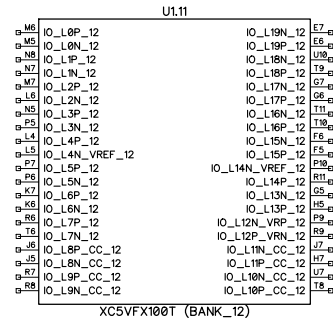
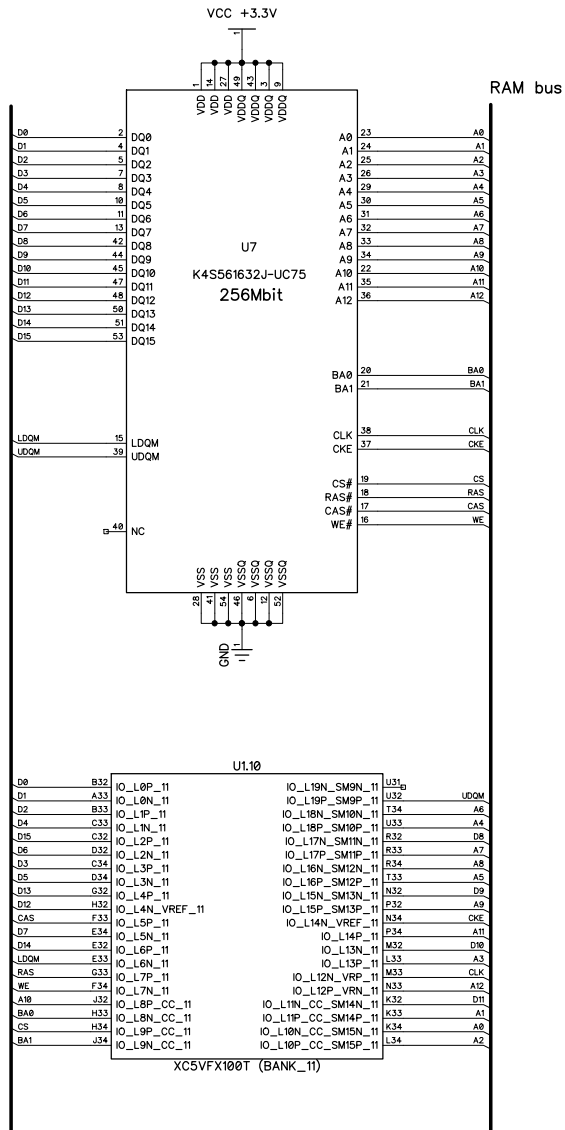
MODEL: XMF5-100-64-256 50/75M Rev.C

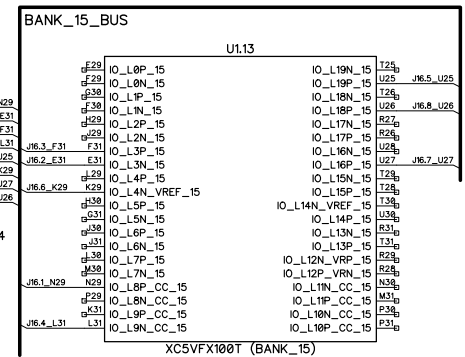
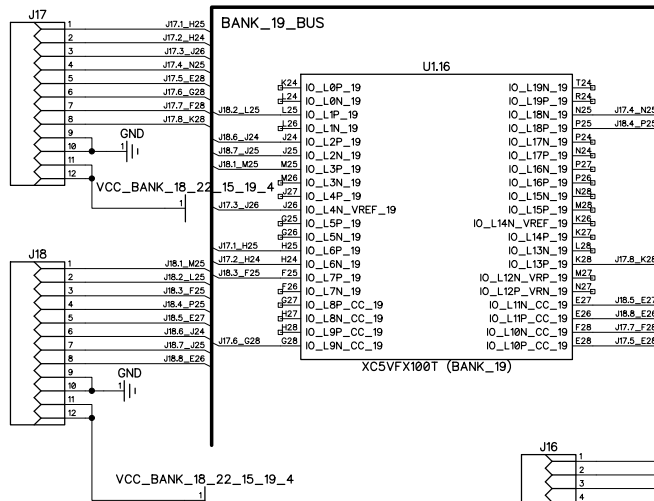
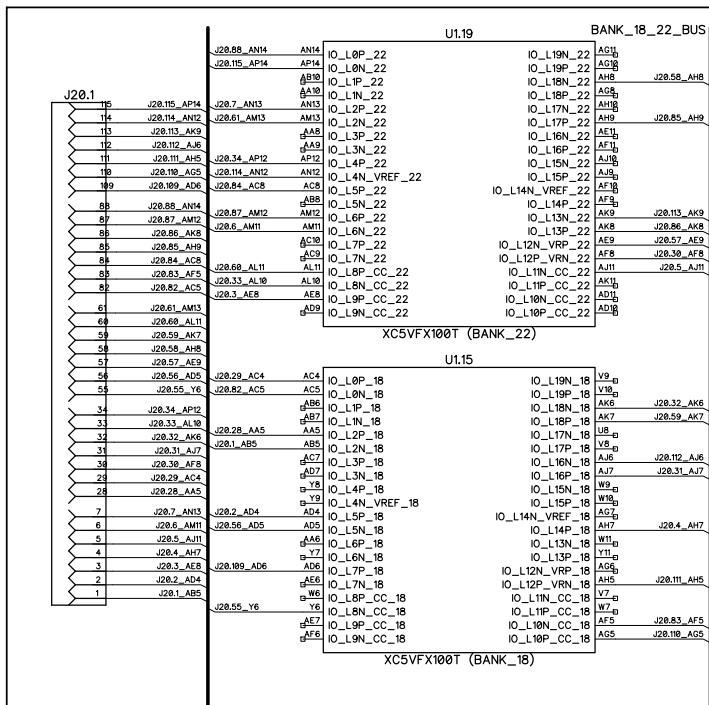


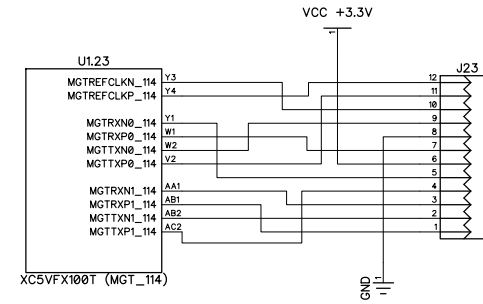
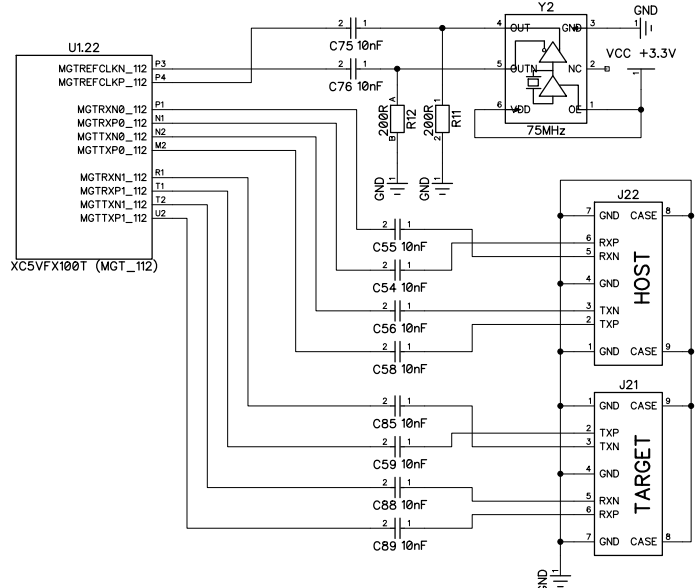
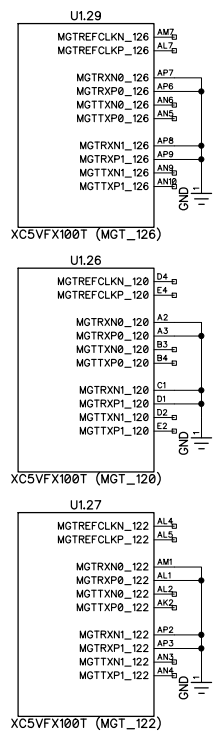
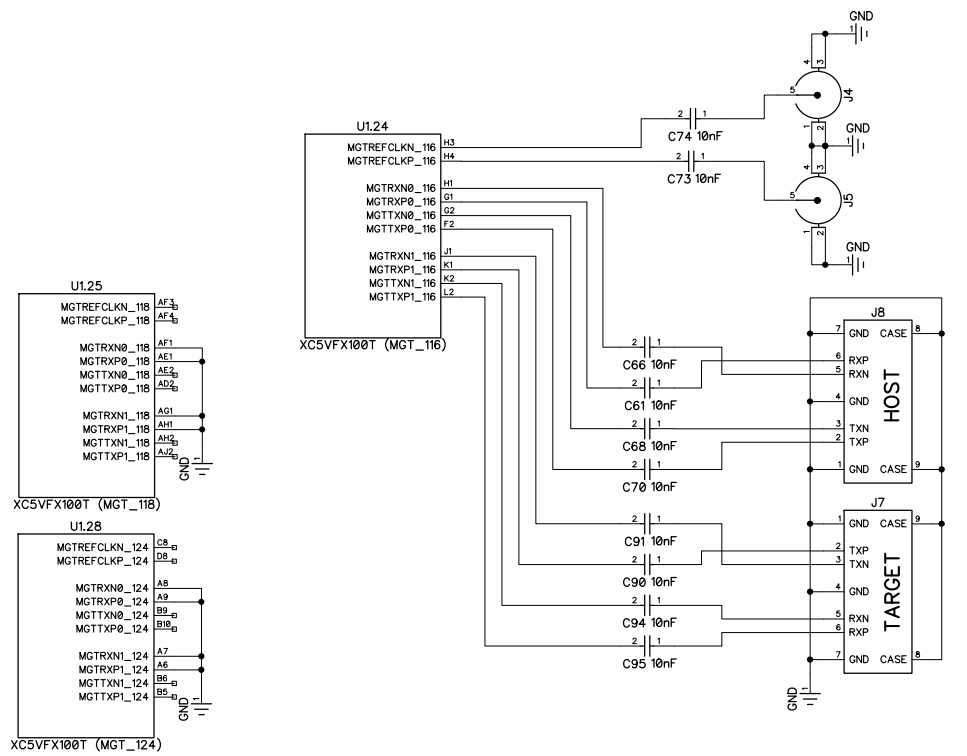




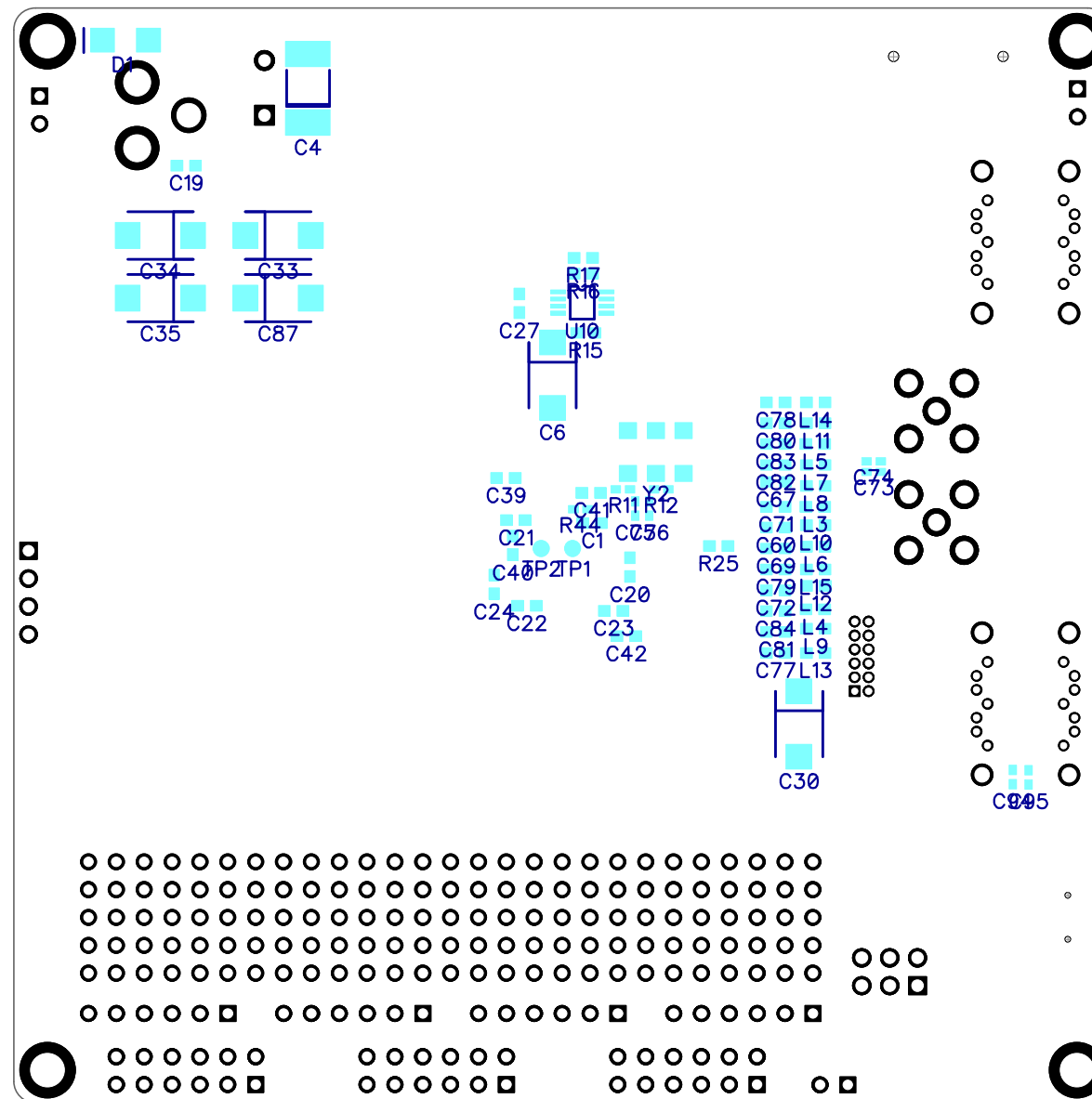
XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5-100-64-256 50/75M	Rev. C	06.08.2021	Sheet 2/5
JTAG, FLASH, CLOCK, RS-232, LEDS, BUTTONS, EEPROM			PLDkit OU www.pldkit.com



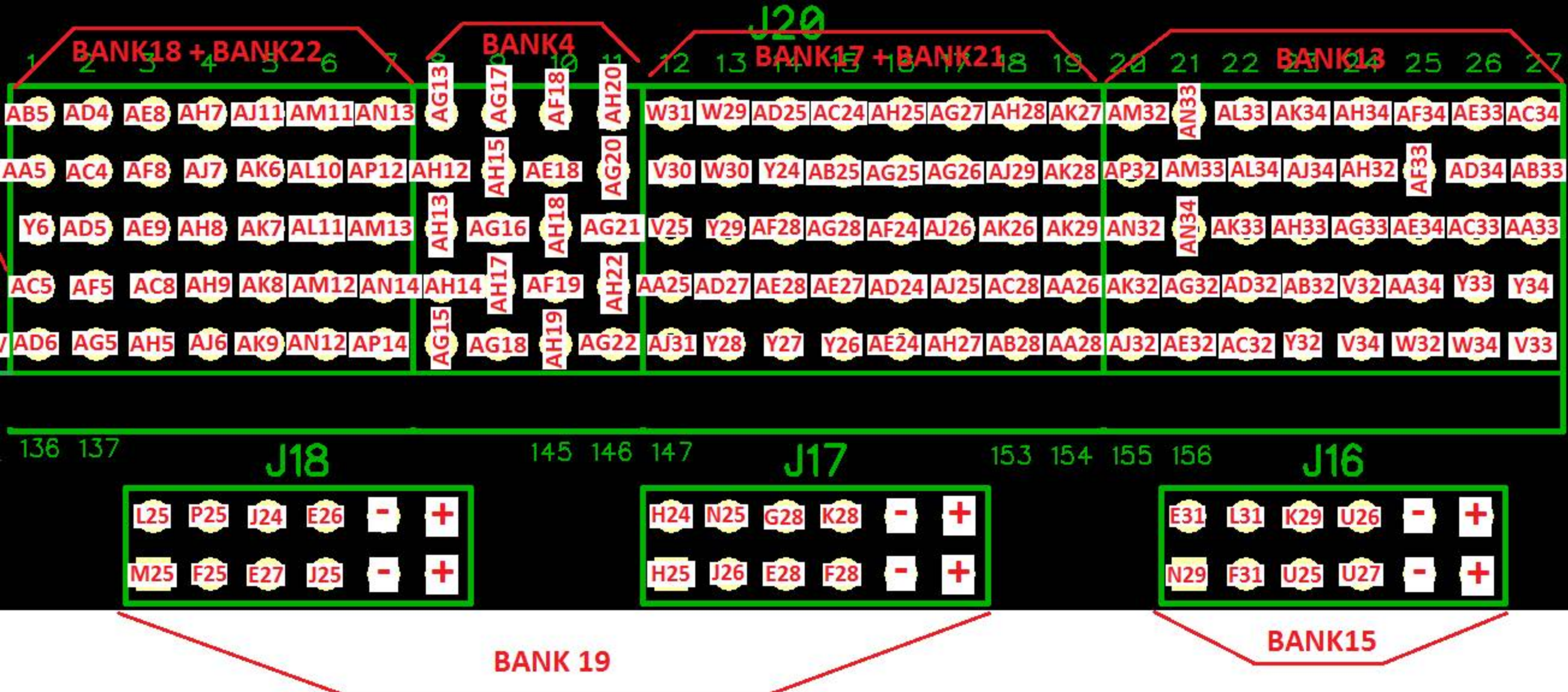


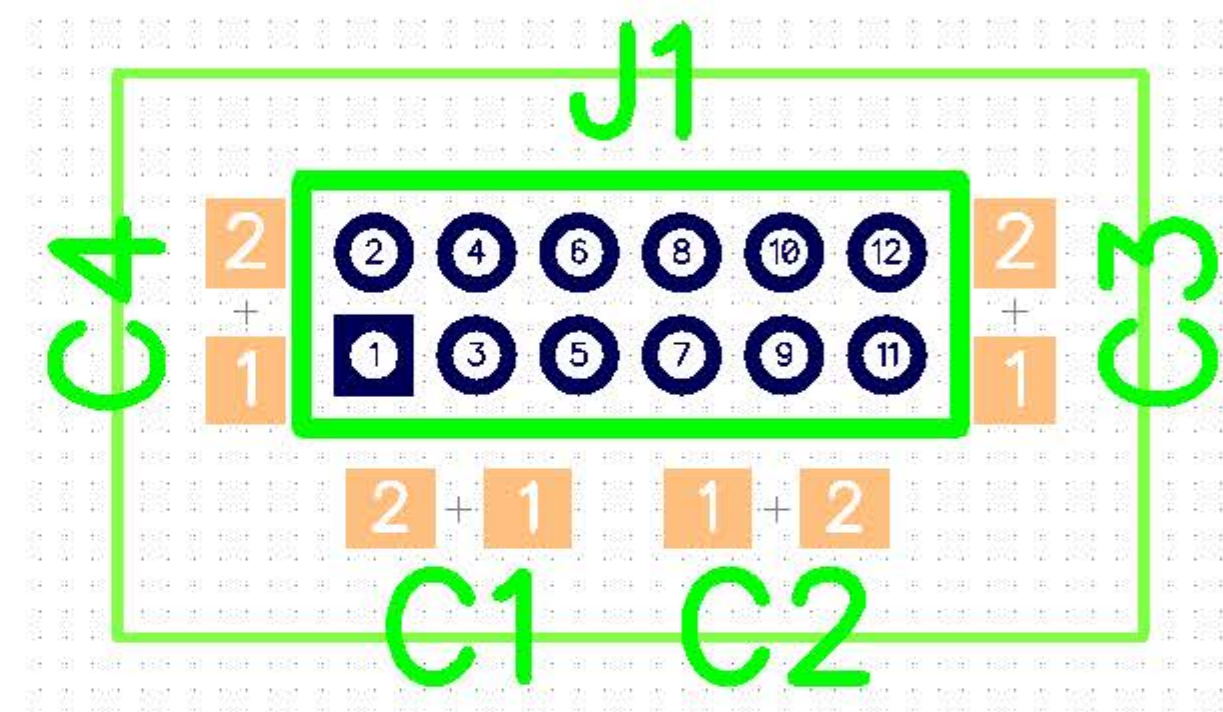
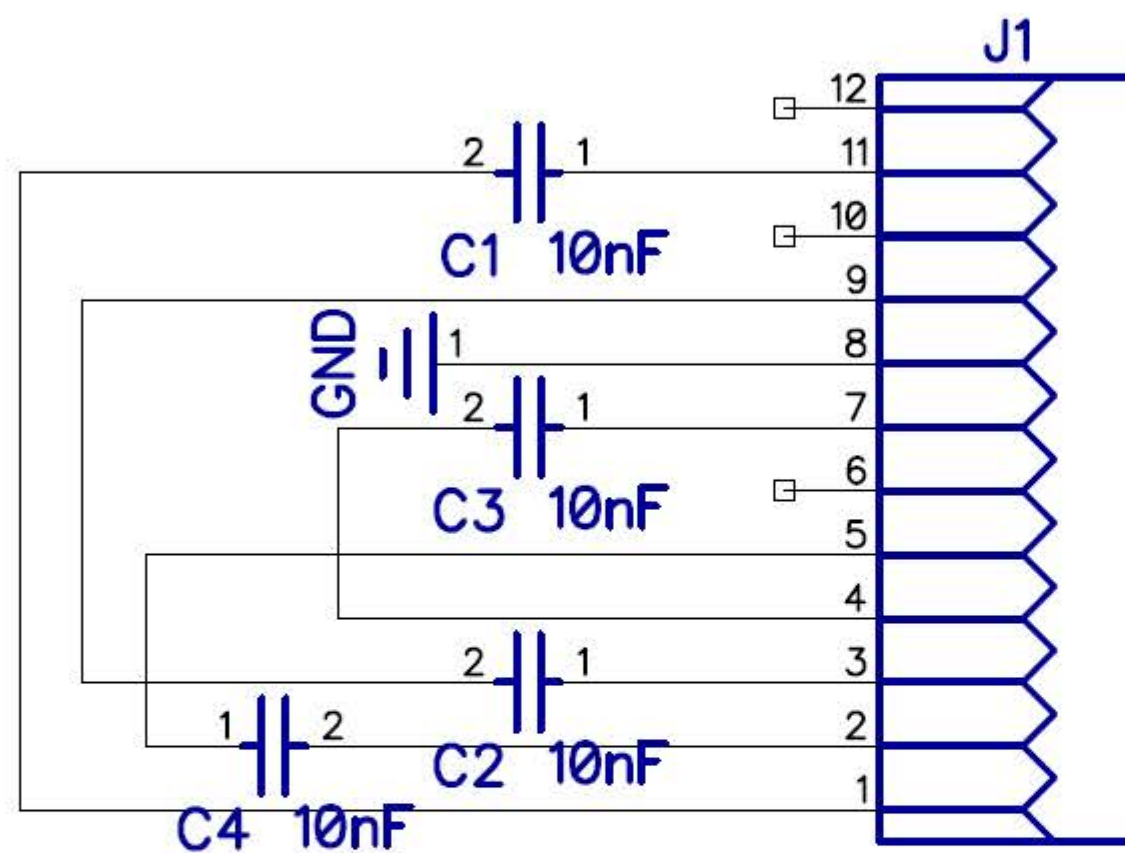


XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5-100-64-256 50/75M	Rev. C	06.08.2021	Sheet 5/5
TRANSCEIVERS			PLDkit OU www.pldkit.com



XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5	Rev. C	05.03.2016	Sheet 1/1
PCB LAYOUT			PLDkit OU www.pldkit.com





J23 GTP LOOP TEST JUMPER FOR XMF5

Rev. A

10.01.2014

Sheet 1/1