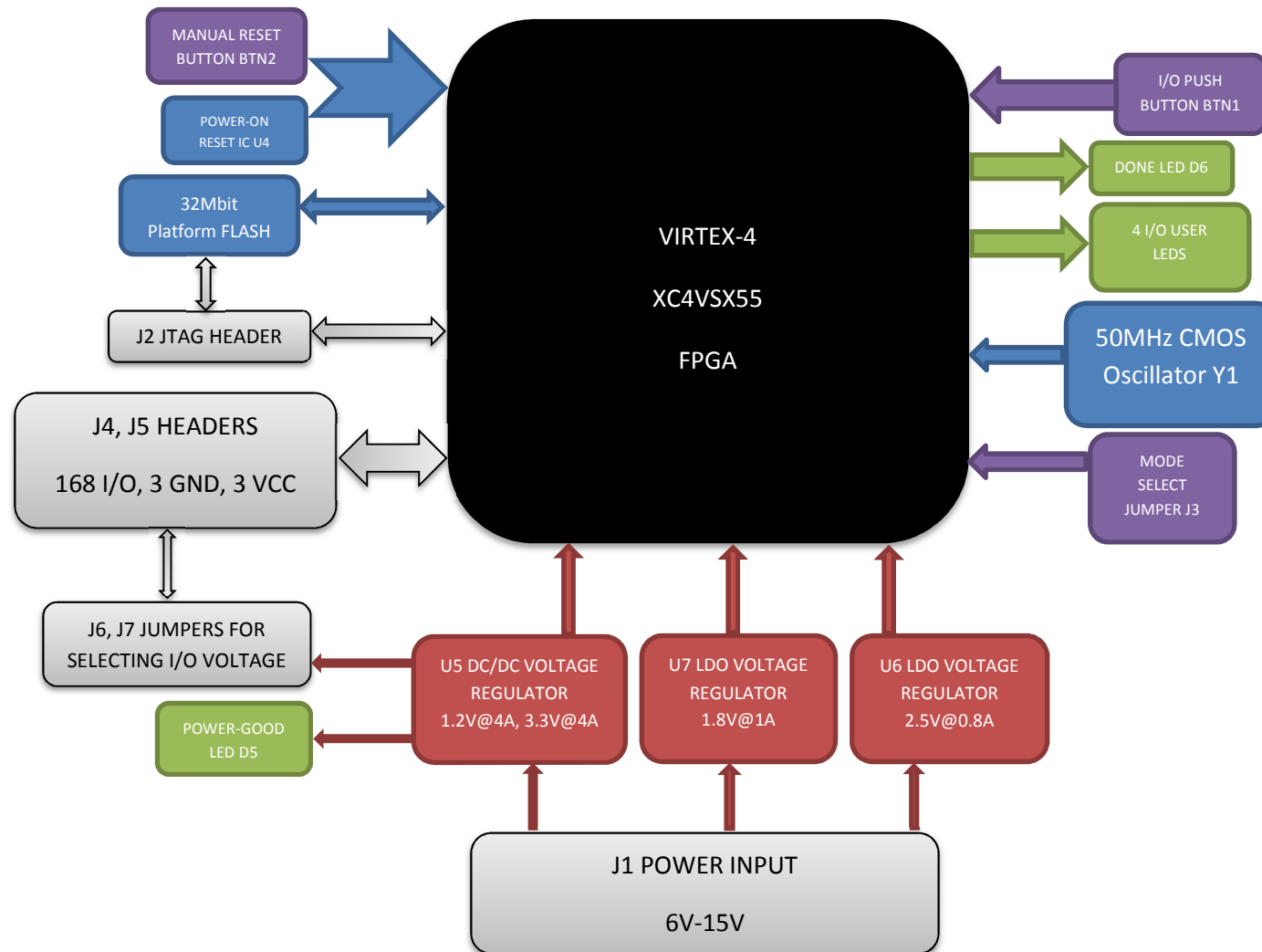


XILINX VIRTEX-4 FPGA MODULE BLOCK SCHEME

MODEL: XMF4-55-32 50M Rev.A

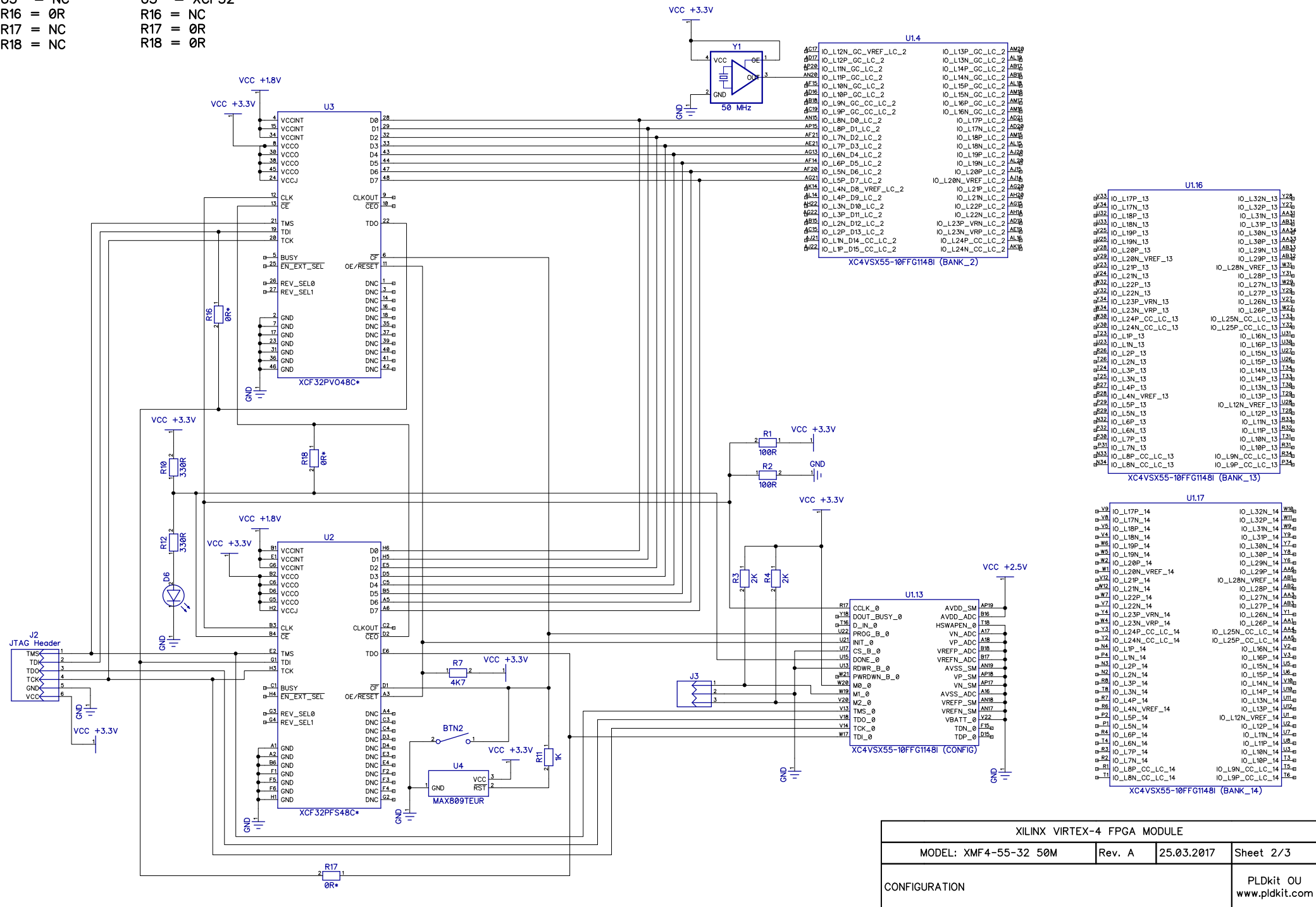


* VERSION 1:

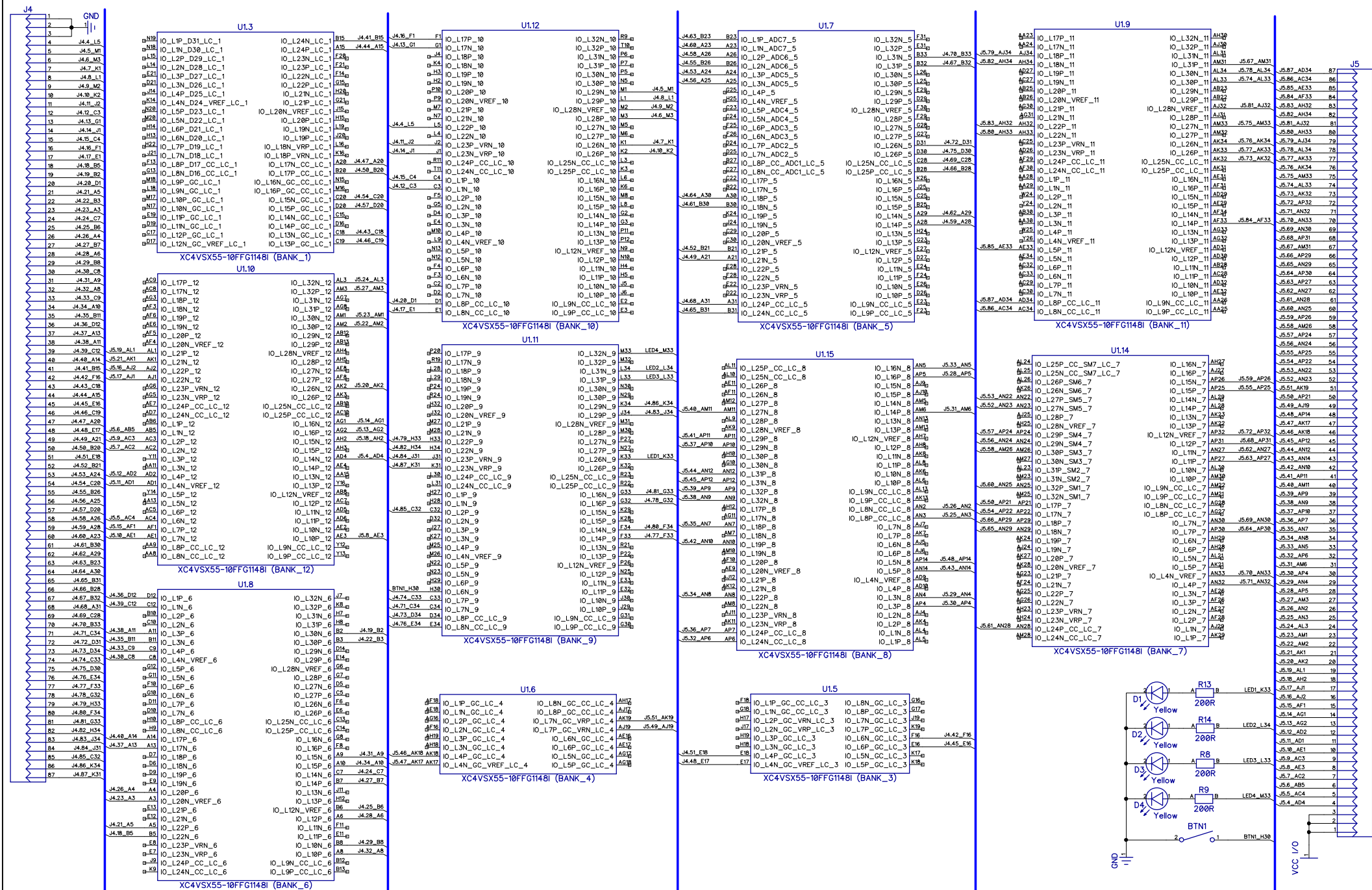
U2 = XCF32
U3 = NC
R16 = 0R
R17 = NC
R18 = NC

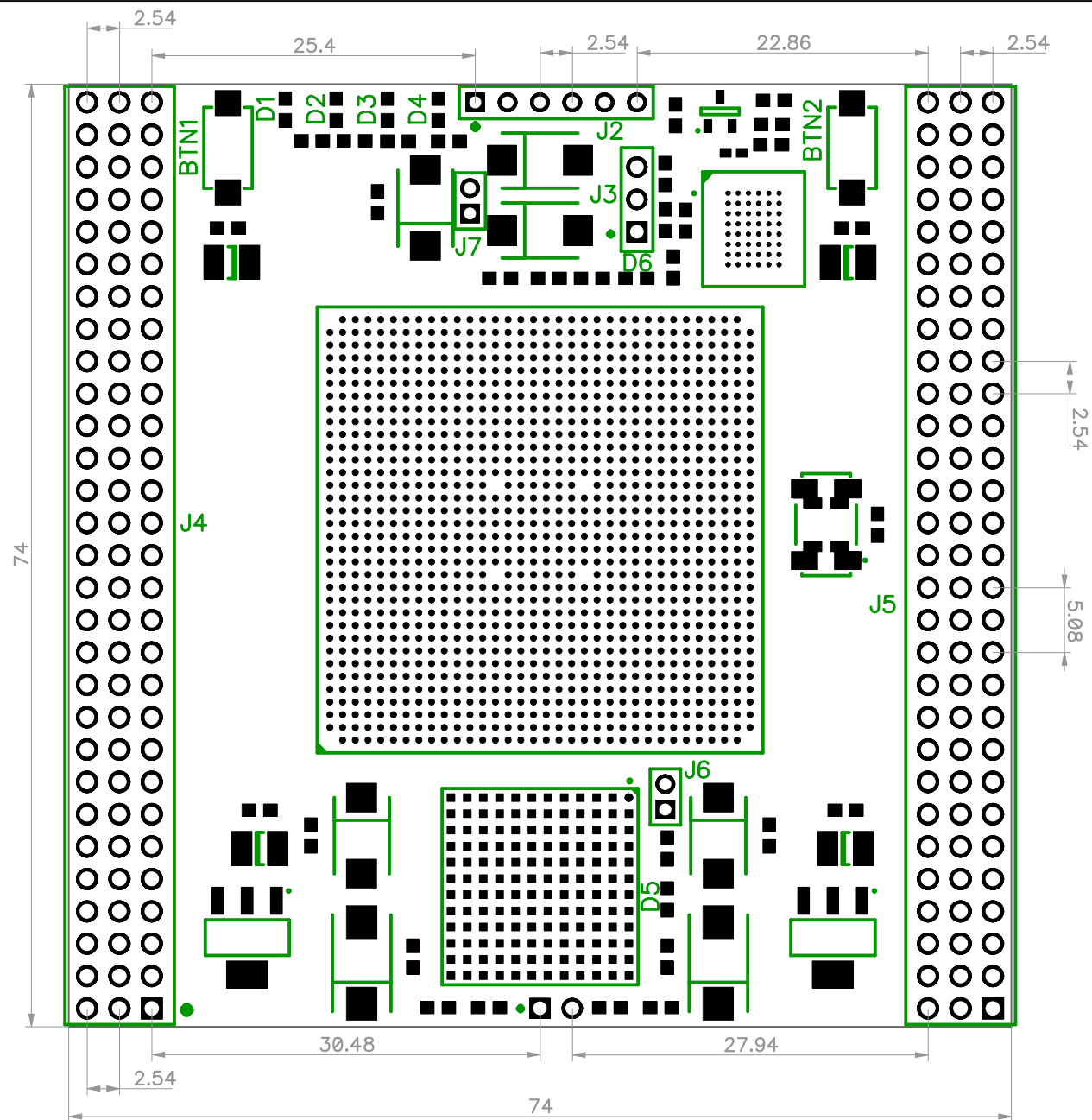
* VERSION 2:

U2 = NC
U3 = XCF32
R16 = NC
R17 = 0R
R18 = 0R



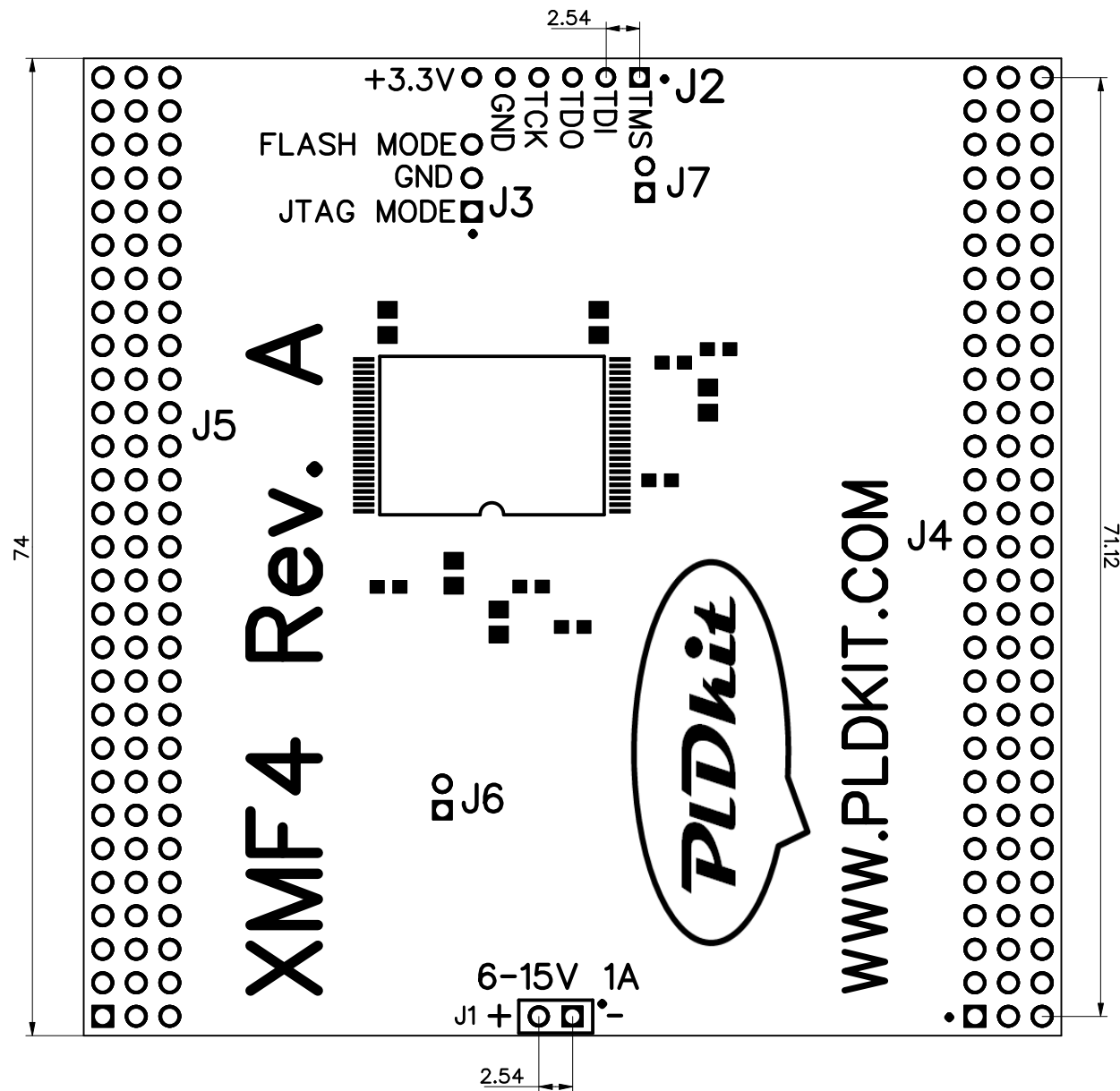
| XILINX VIRTEX-4 FPGA MODULE | | | |
|-----------------------------|--------|------------|-----------------------------|
| MODEL: XMF4-55-32 50M | Rev. A | 25.03.2017 | Sheet 2/3 |
| CONFIGURATION | | | PLDkit OU www.pldkit.com |





All dimensions in mm

| XILINX VIRTEX-4 FPGA MODULE | | | |
|-----------------------------|--------|------------|-----------------------------|
| MODEL: XMF4 | Rev. A | 25.03.2017 | Sheet 1/1 |
| PCB LAYOUT | | | PLDkit OU www.pldkit.com |



All dimensions in mm

| XILINX VIRTEX-4 FPGA MODULE | | | |
|-----------------------------|--------|------------|-----------------------------|
| MODEL: XMF4 | Rev. A | 25.03.2017 | Sheet 1/1 |
| PCB LAYOUT | | | PLDkit OU www.pldkit.com |

