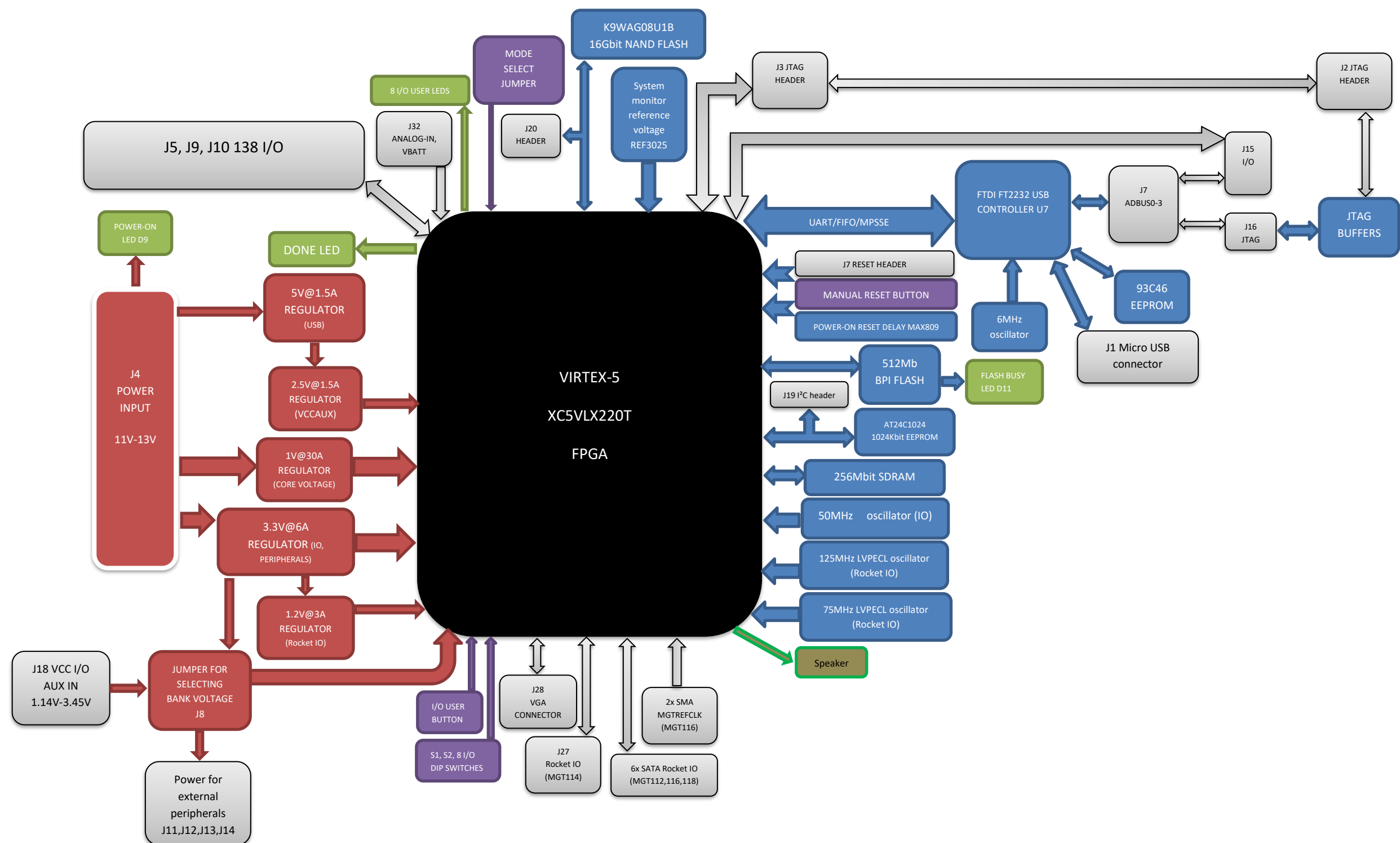
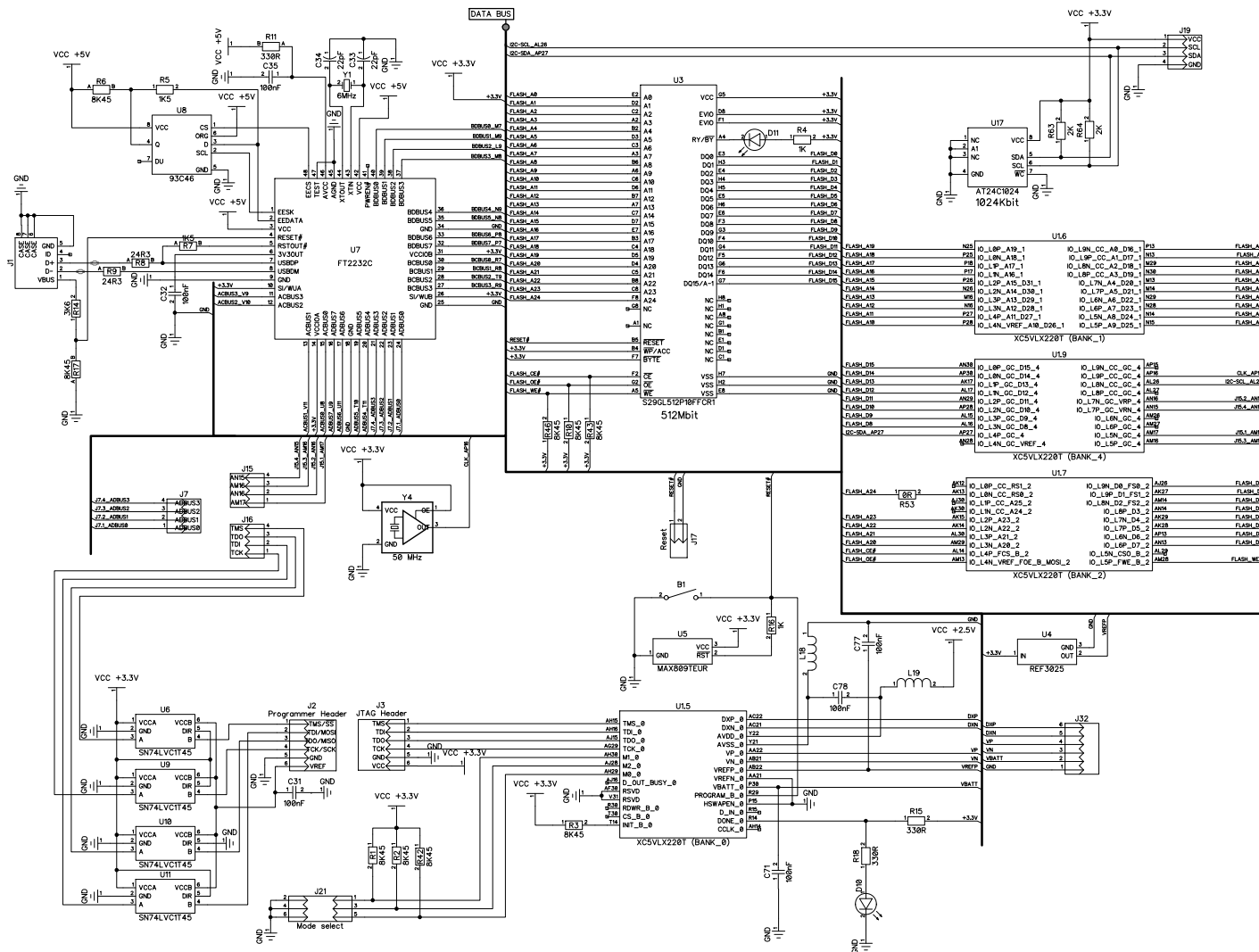


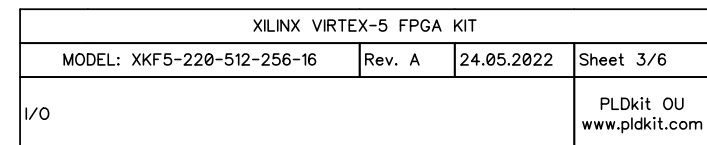
## XILINX VIRTEX-5 FPGA KIT BLOCK SCHEME

MODEL: XKF5-220-512-256-16 Rev.A

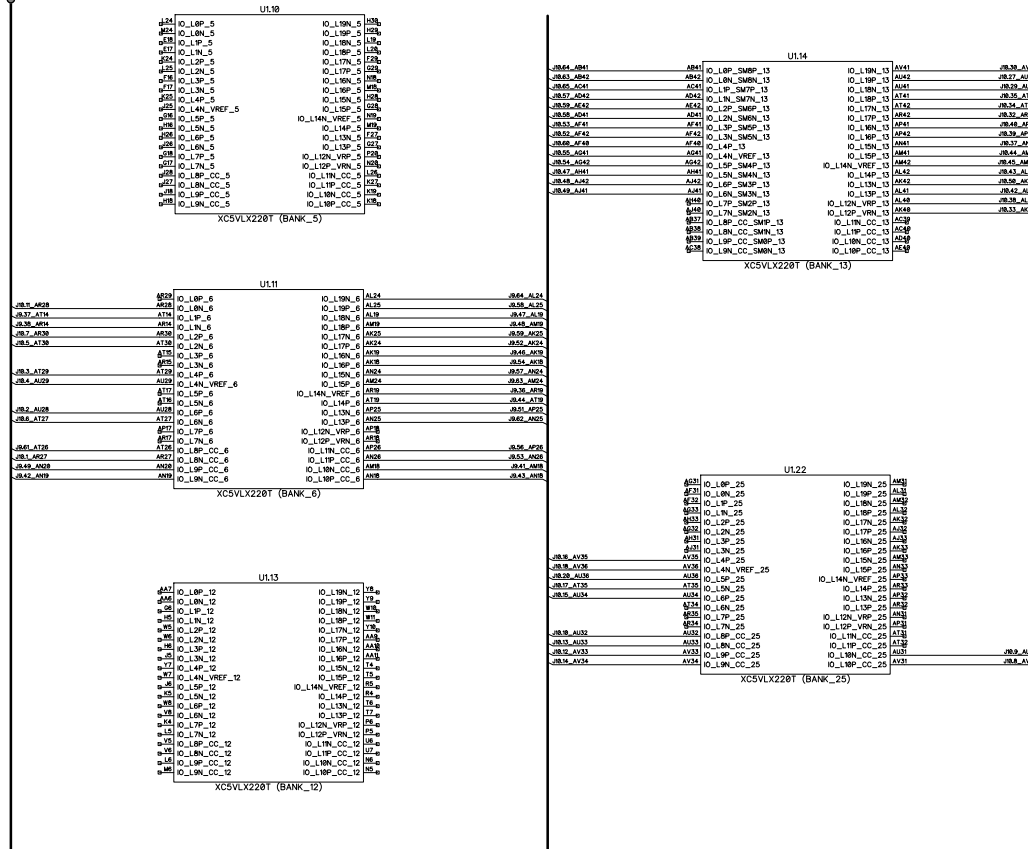








DATA BUS

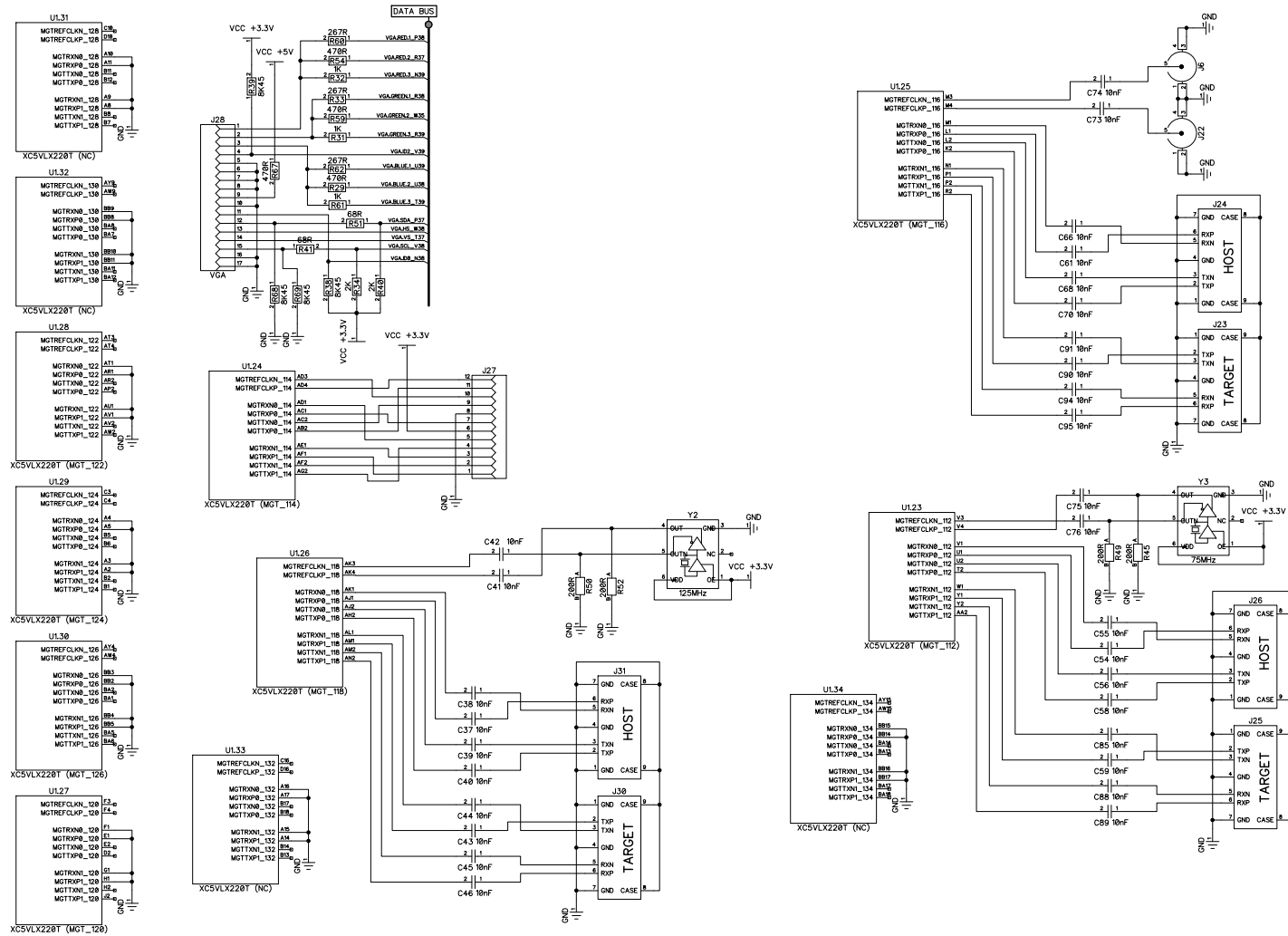


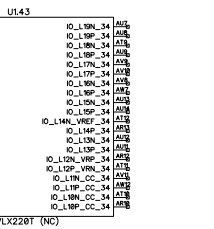
XILINX VIRTEX-5 FPGA KIT

MODEL: XKF5-220-512-256-16    Rev. A    24.05.2022    Sheet 4/6

I/O

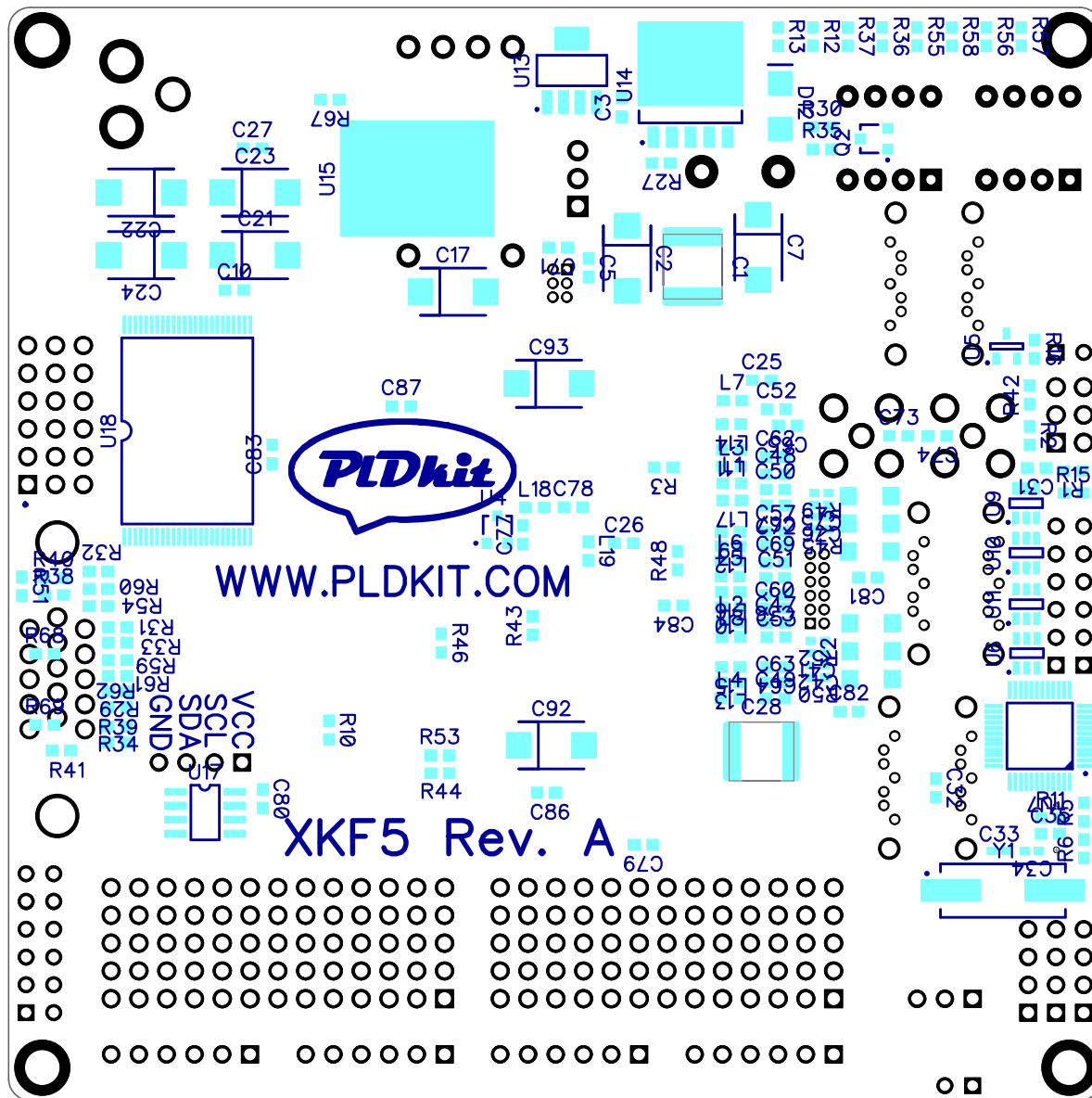
PLDkit OU  
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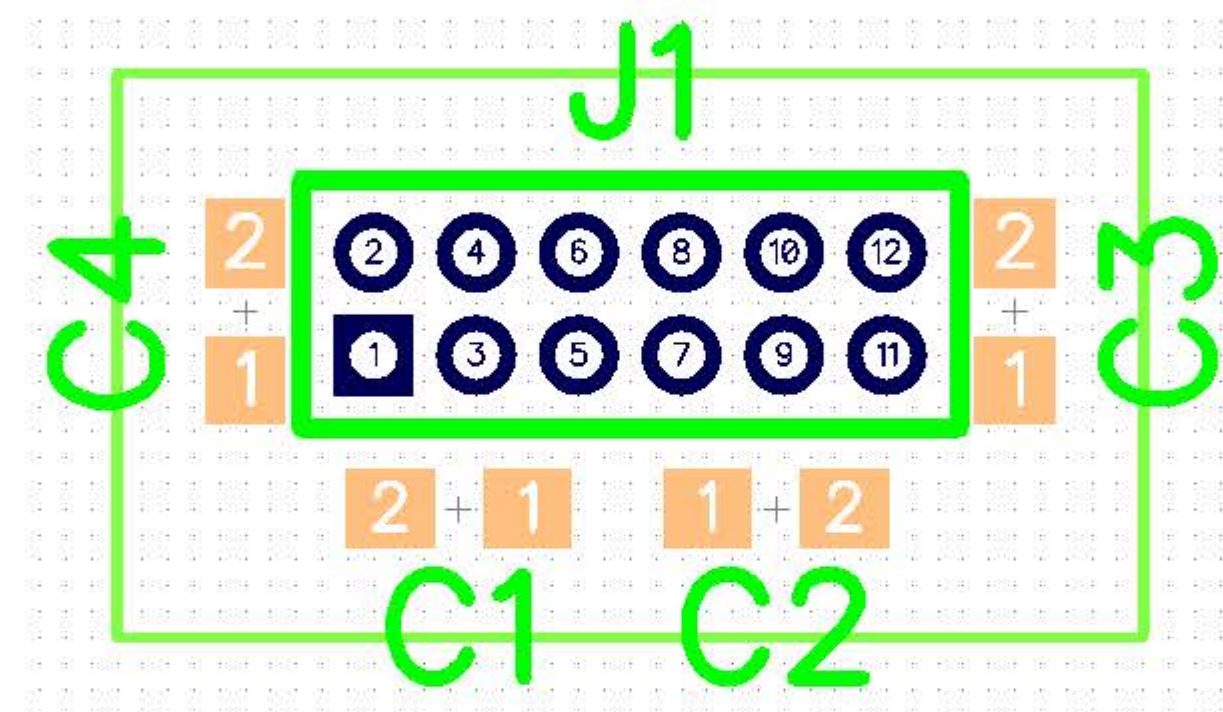
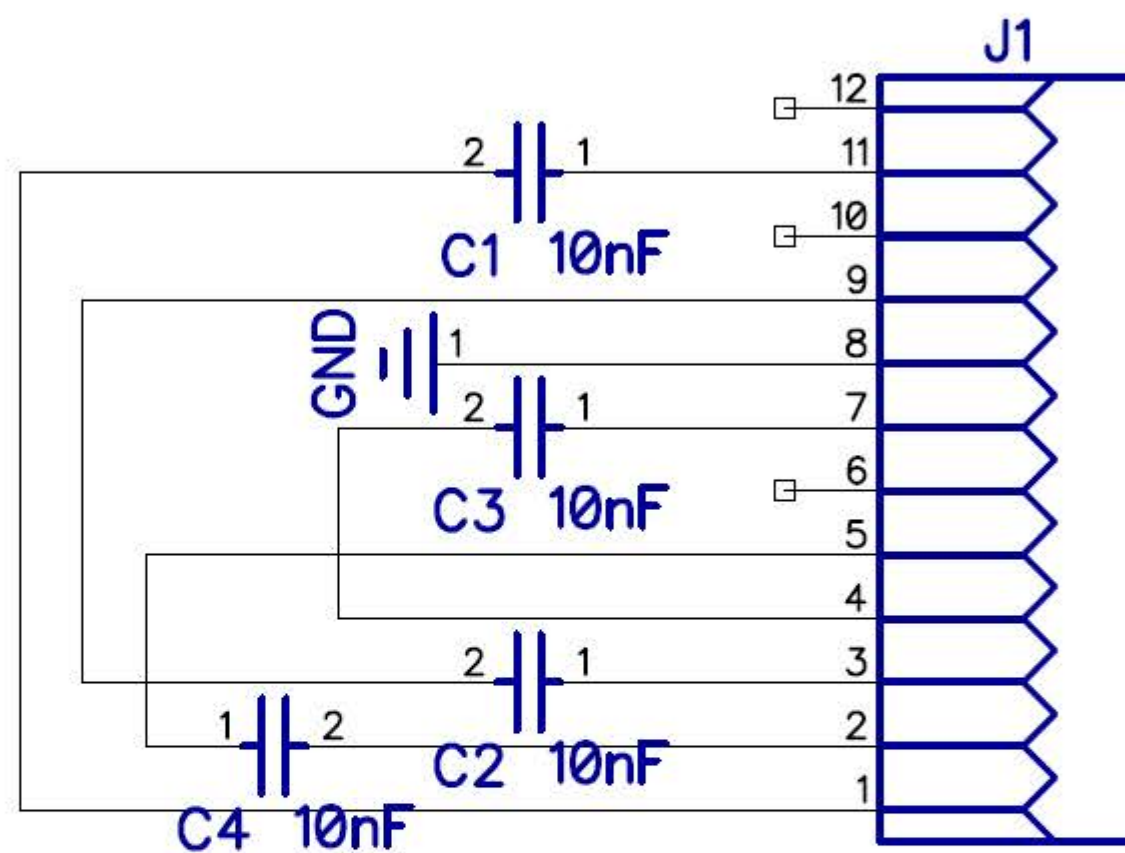








XILINX VIRTEX-5 FPGA KIT			
MODEL: XKF5	Rev. A	17.01.2021	Sheet 1/1
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GTP LOOP TEST JUMPER				
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