XILINX VIRTEX-5 FPGA KIT BLOCK SCHEME

MODEL: XKF5-110-512-256-16 Rev.A

XILINX VIRTEX-5 FPGA KIT			
MODEL: XKF5-110-512-256-16	Rev. A	26.05.2021	Sheet 1/6
POWER			PLDkit OU www.pldkit.com

XILINX VIRTEX-5 FPGA KIT			
MODEL: XKF5-110-512-256-16	Rev. A	26.05.2021	Sheet 2/6
CONFIGURATION			PLDkit OU www.pldkit.com

XILINX VIRTEX-5 FPGA KIT			
MODEL: XKF5-110-512-256-16	Rev. A	26.05.2021	Sheet 3/6
I/O			PLDkit OU www.pldkit.com

XILINX VIRTEX-5 FPGA KIT			
MODEL: XKF5-110-512-256-16	Rev. A	26.05.2021	Sheet 4/6
I/O			PLDkit OU www.pldkit.com

XILINX VIRTEX-5 FPGA KIT			
MODEL: XKF5-110-512-256-16	Rev. A	26.05.2021	Sheet 5/6
TRANSCEIVERS, VGA			PLDkit OU www.pldkit.com

XILINX VIRTEX-5 FPGA KIT			
MODEL: XKF5-110-512-256-16	Rev. A	26.05.2021	Sheet 6/6
RAM, NAND			PLDkit OU www.pldkit.com

Requirements for DC Power Plug

All dimensions in mm

XILINX VIRTEX-5 FPGA KIT			
MODEL: XKF5	Rev. A	17.01.2021	Sheet 1/1
PCB LAYOUT			PLDkit OU www.pldkit.com

XILINX VIRTEX-5 FPGA KIT			
MODEL: XKF5	Rev. A	17.01.2021	Sheet 1/1
PCB LAYOUT			PLDkit OU www.pldkit.com

GTP LOOP TEST JUMPER				
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