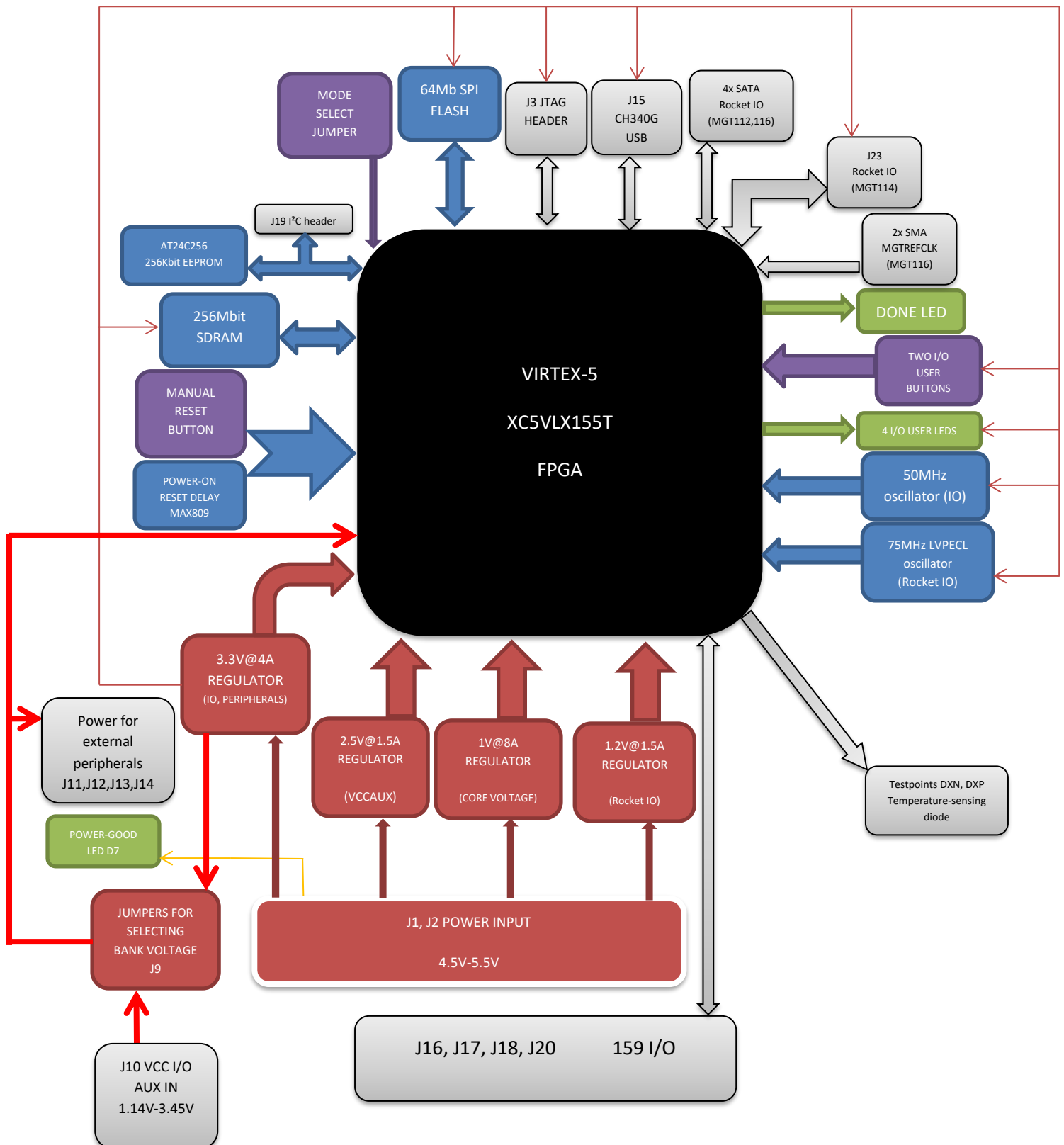
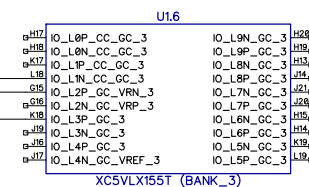
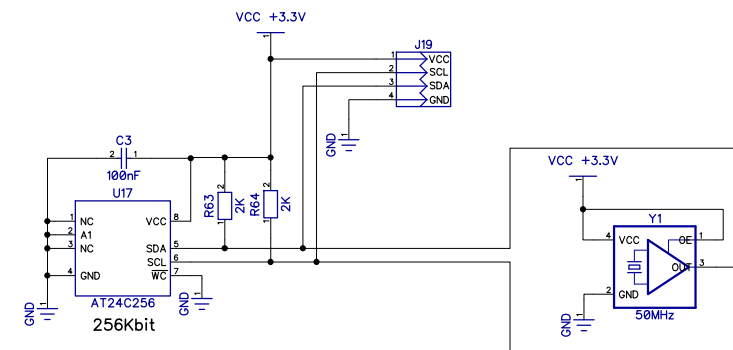
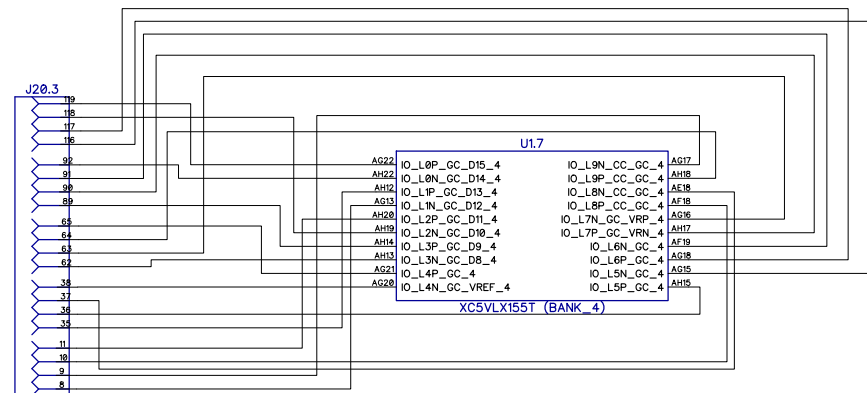
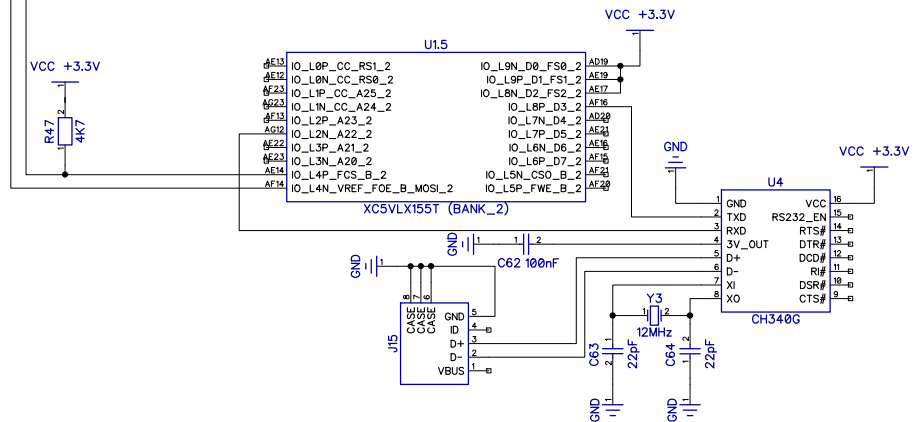
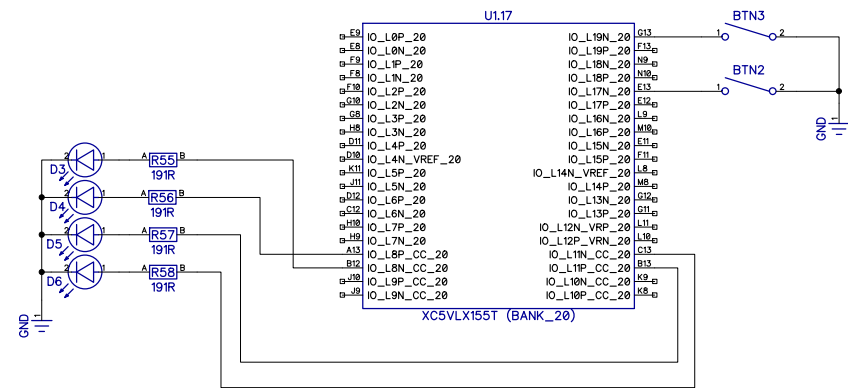
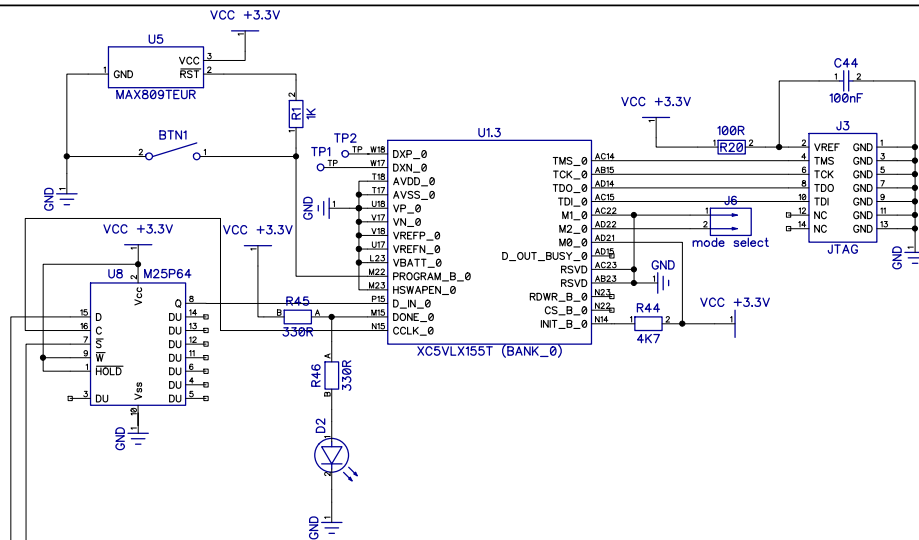


XILINX VIRTEX-5 FPGA MODULE BLOCK SCHEME

MODEL: XMF5-155-64-256 50/75M Rev.C

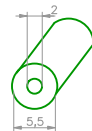




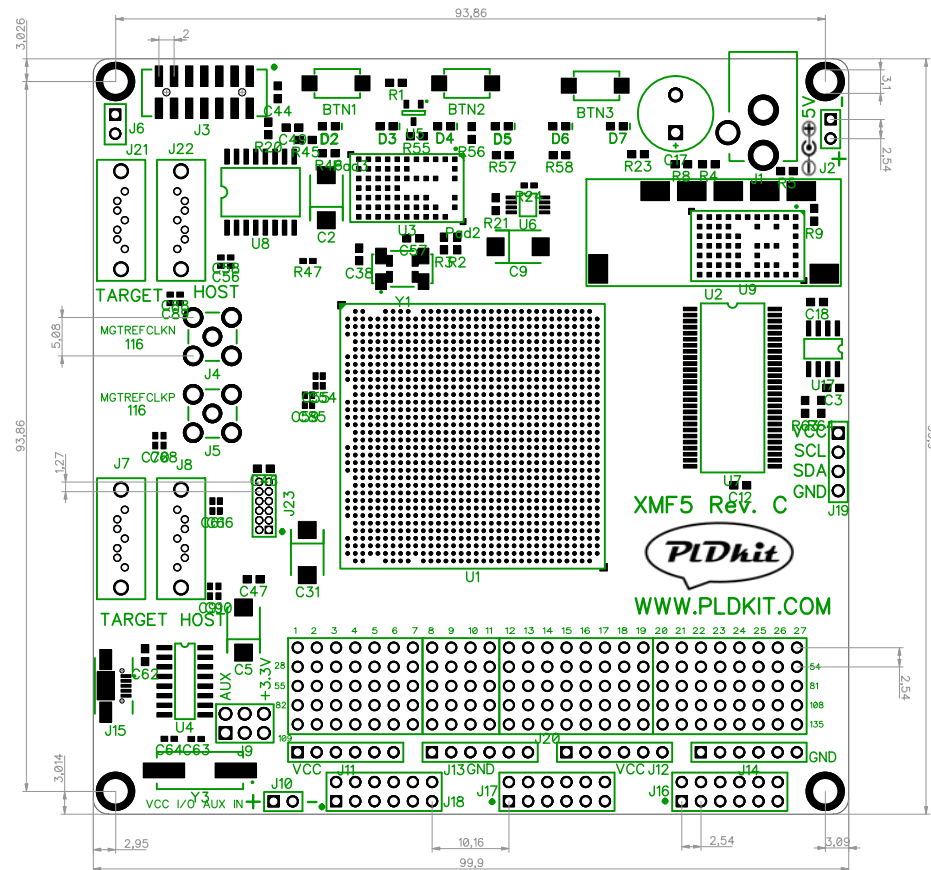
XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5-155-64-256 50/75M	Rev. C	29.03.2019	Sheet 2/5
JTAG, FLASH, CLOCK, USB, LEDS, BUTTONS, EEPROM			PLDkit OU www.pldkit.com

XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5-155-64-256 50/75M	Rev. C	04.12.2019	Sheet 3/5
RAM			PLDkit OU www.pldkit.com

4.5V-5.5V

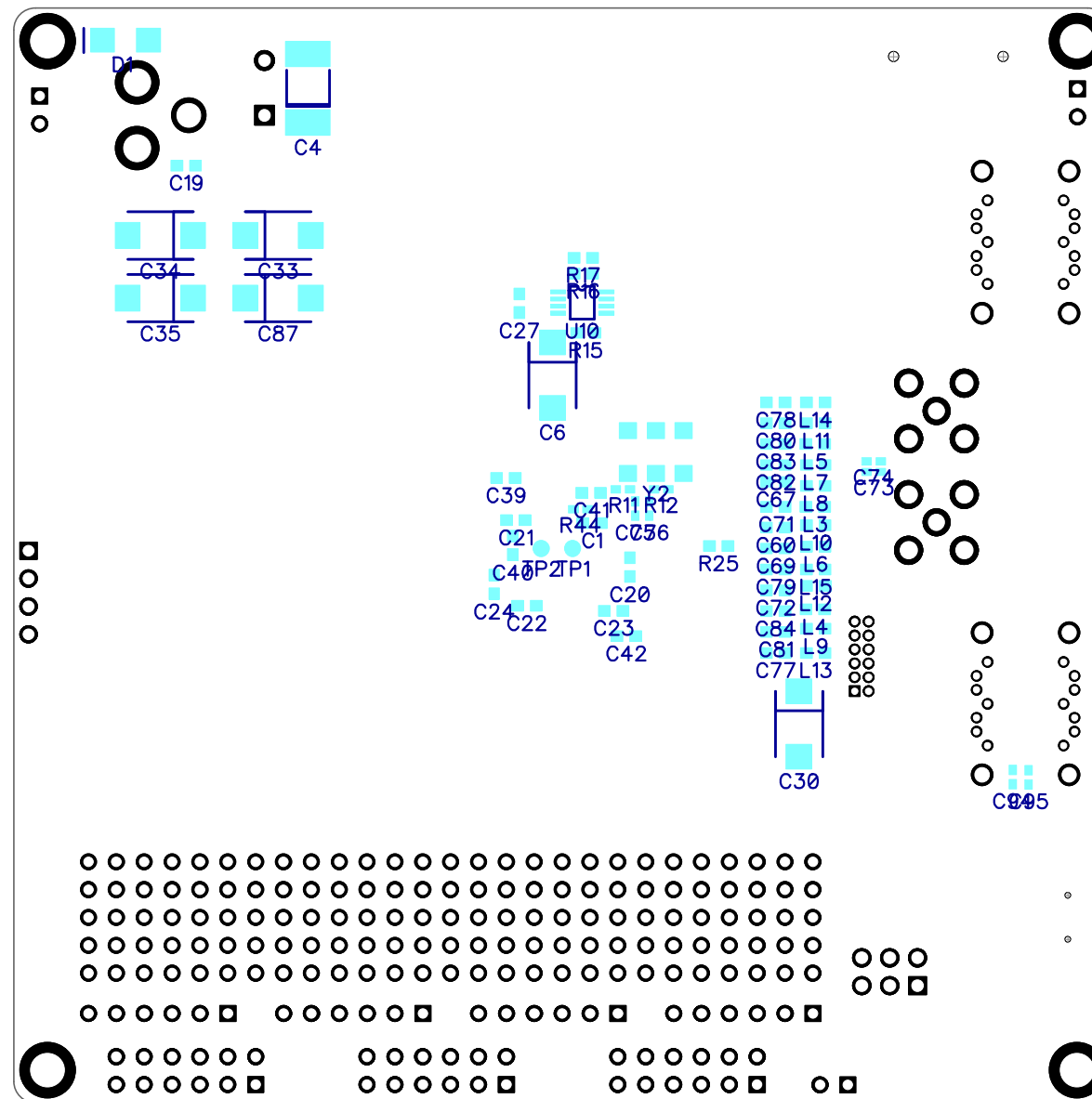


Requirements for
DC Power Plug

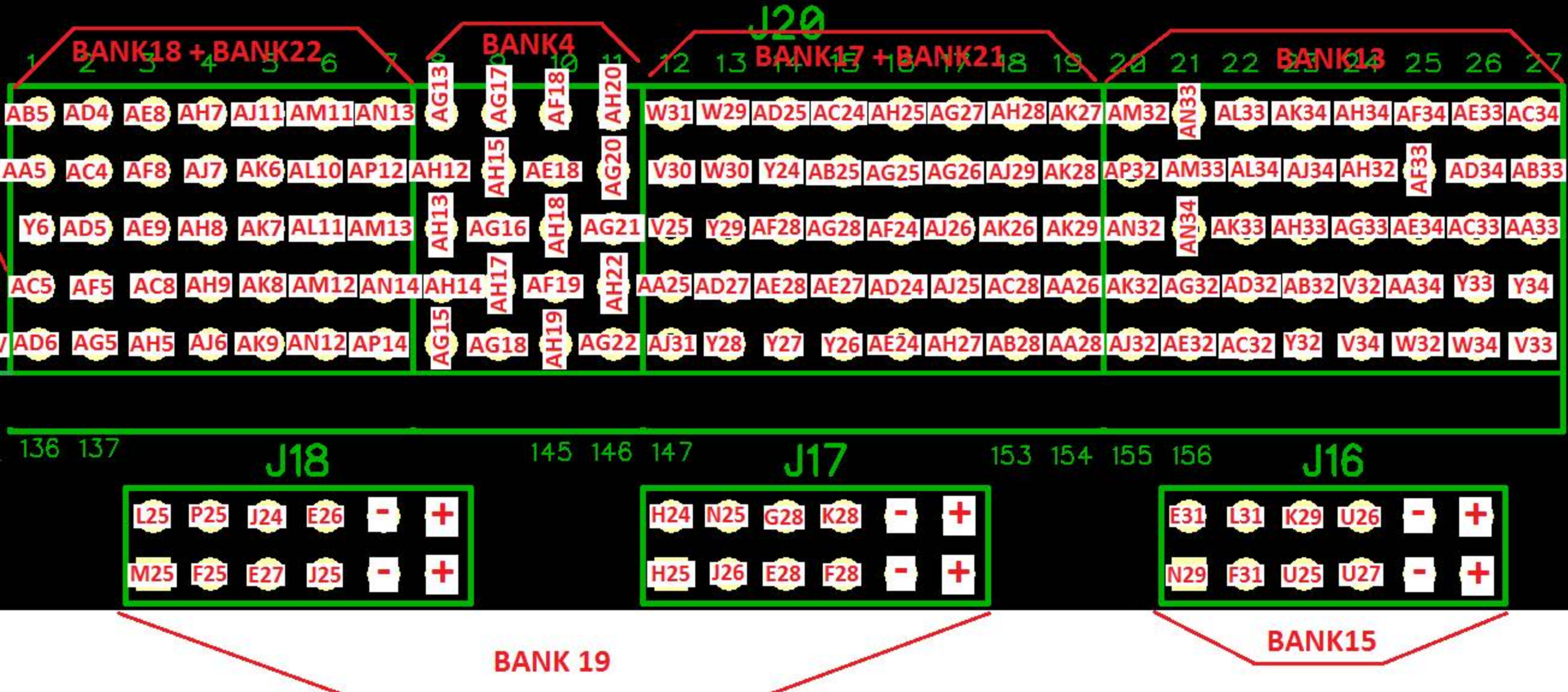


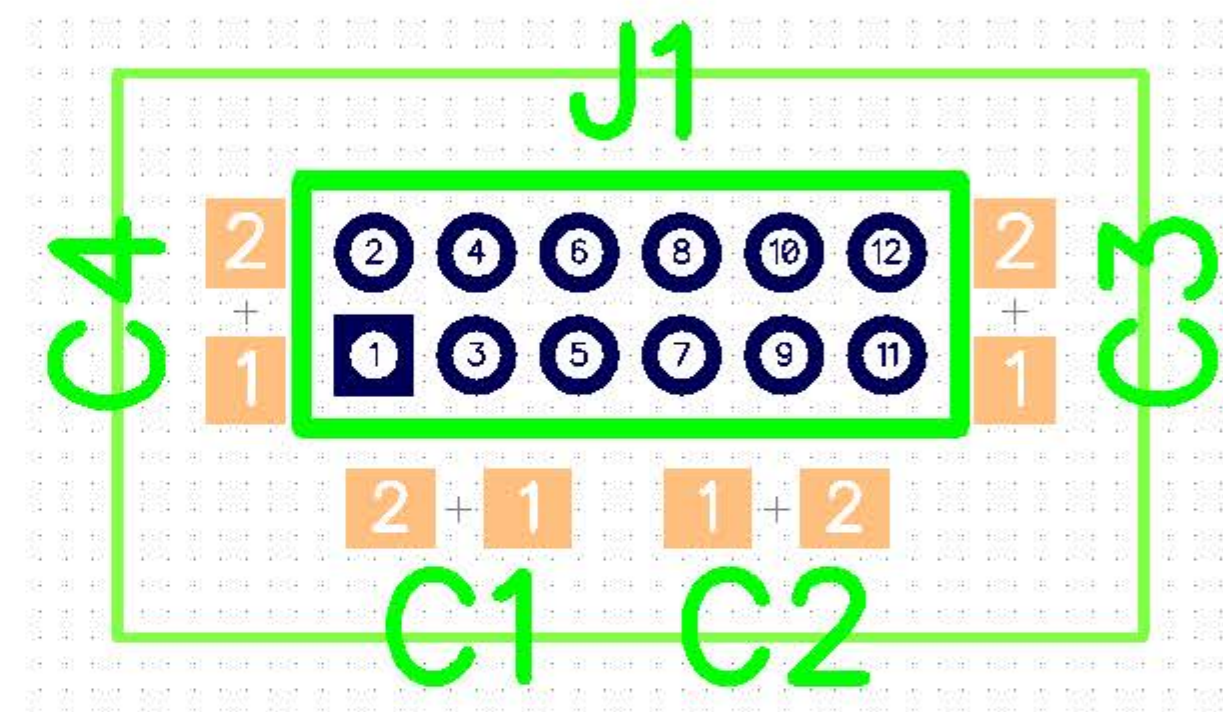
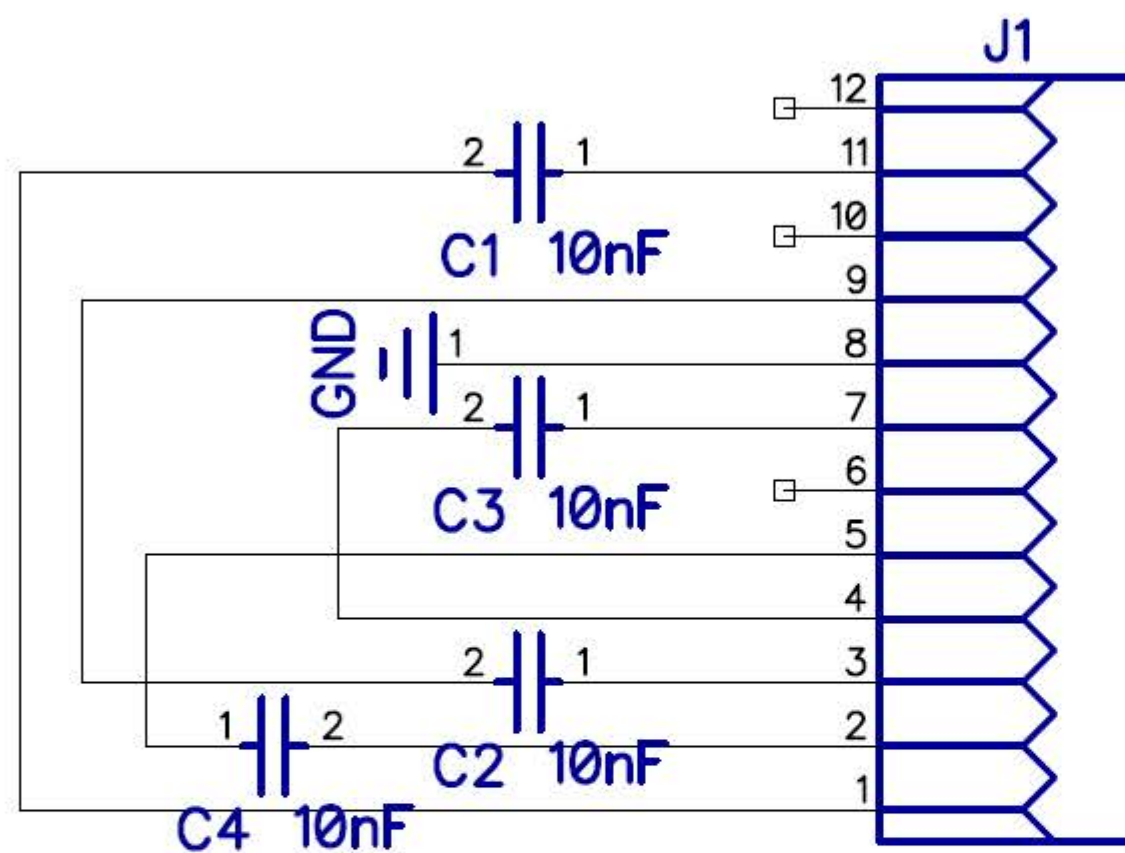
All dimensions in mm

XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5	Rev. C	05.03.2016	Sheet 1/1
PCB LAYOUT			PLDkit OU www.pldkit.com



XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5	Rev. C	05.03.2016	Sheet 1/1
PCB LAYOUT			PLDkit OU www.pldkit.com





J23 GTP LOOP TEST JUMPER FOR XMF5

Rev. A

10.01.2014

Sheet 1/1