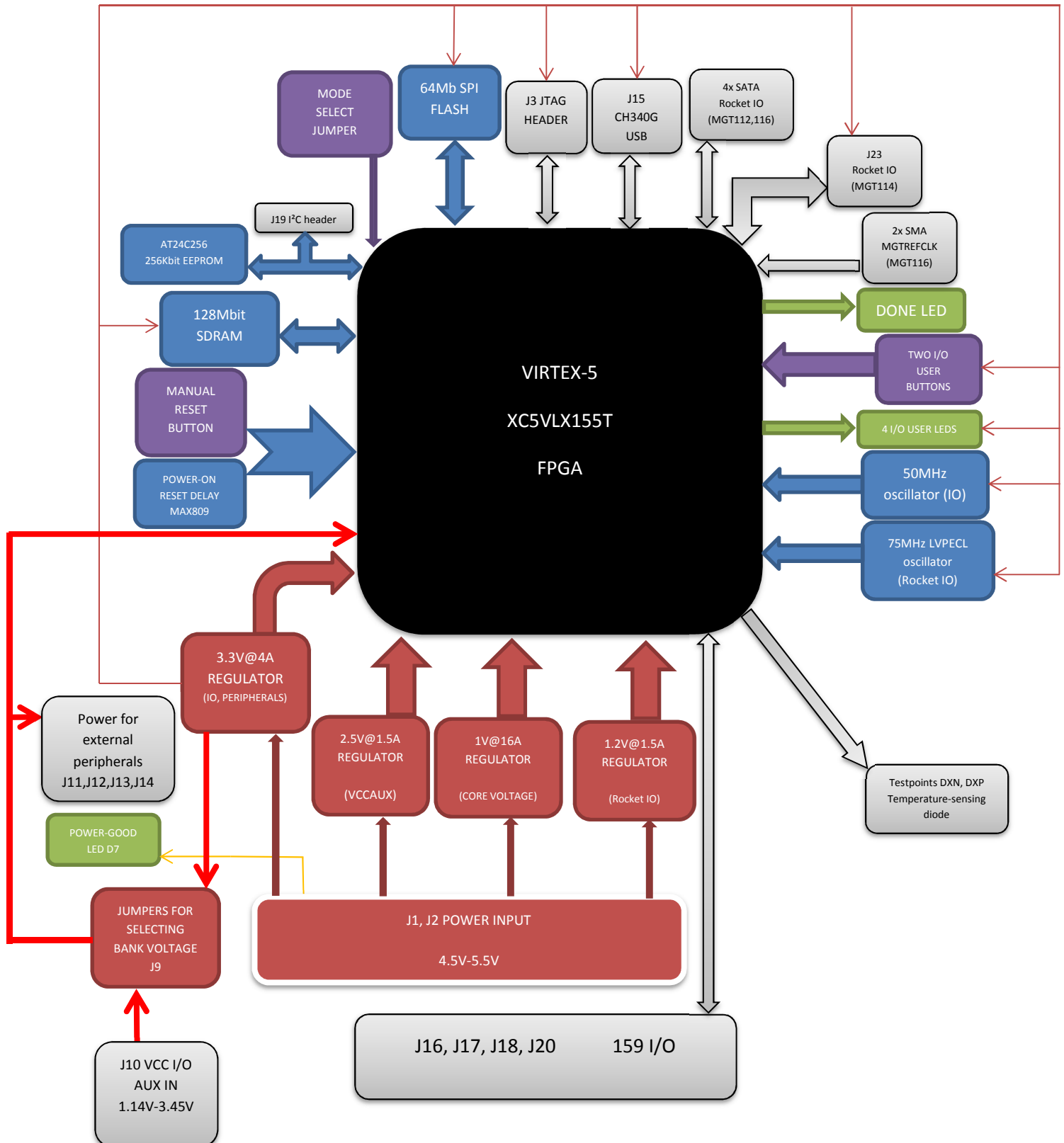
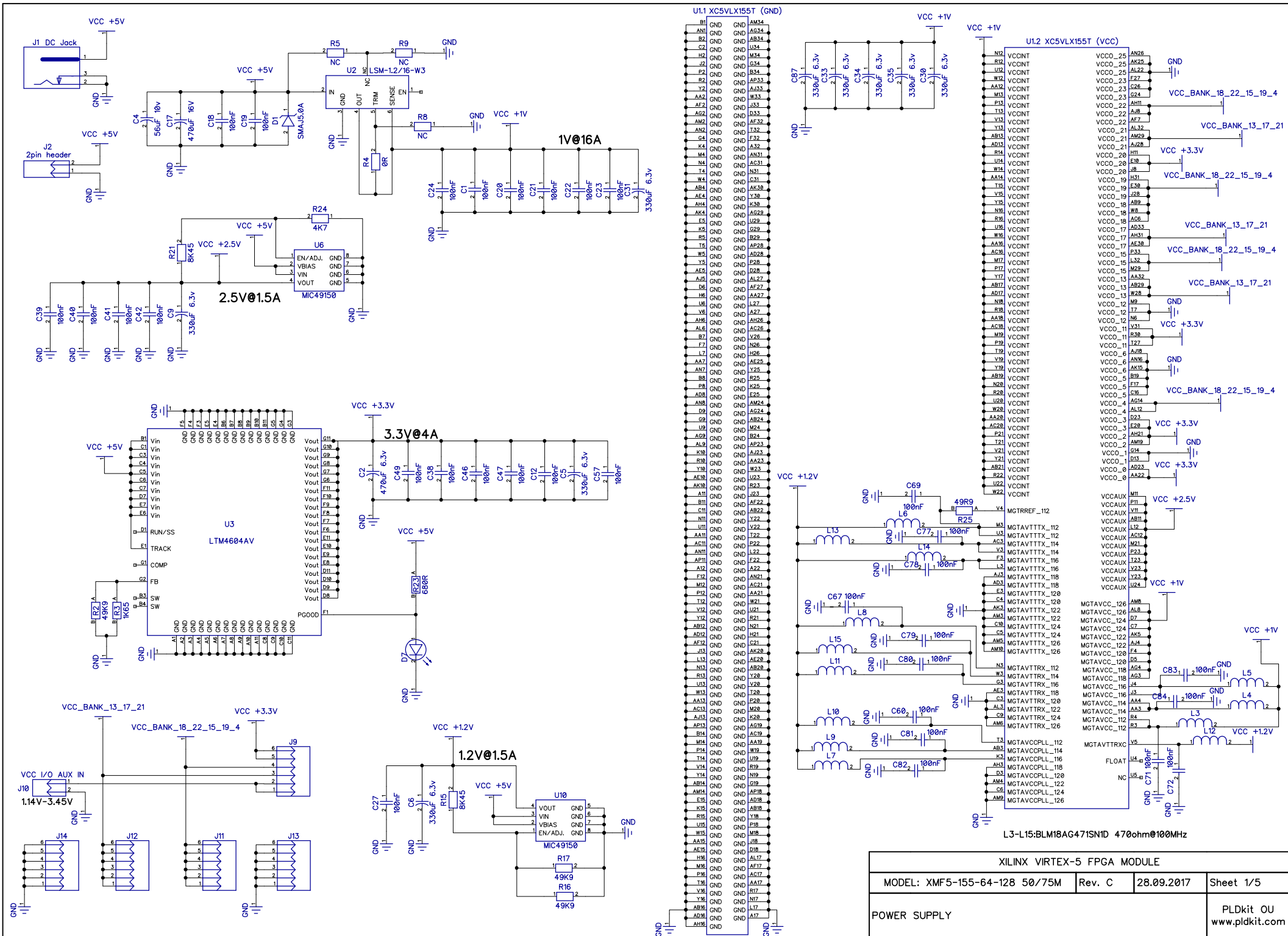


## XILINX VIRTEX-5 FPGA MODULE BLOCK SCHEME

MODEL: XMF5-155-64-128 50/75M Rev.C

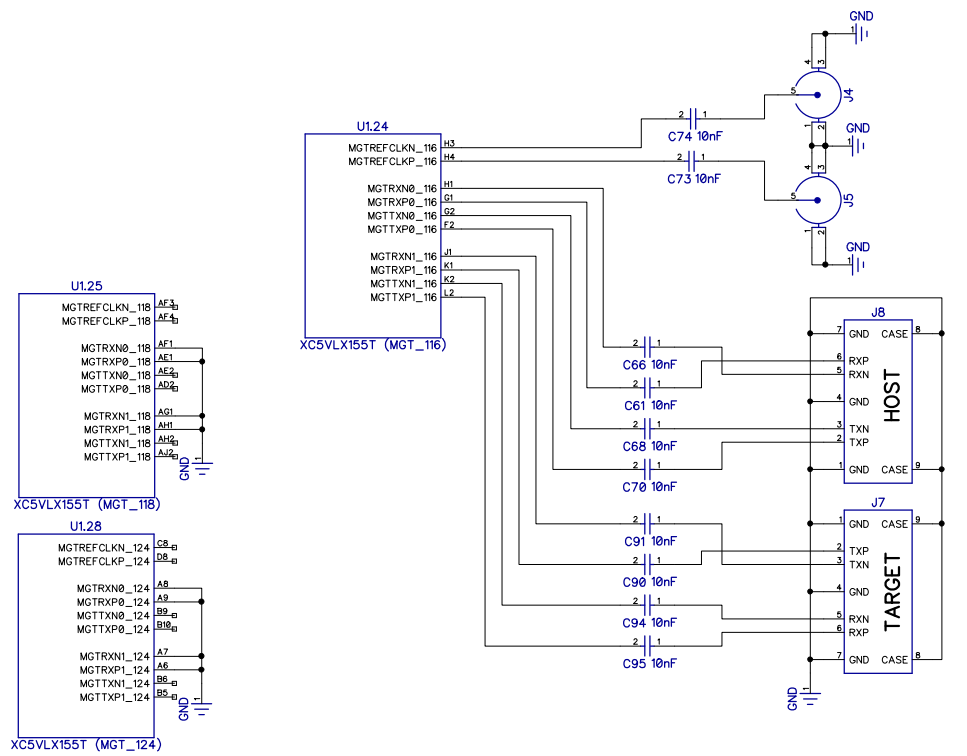




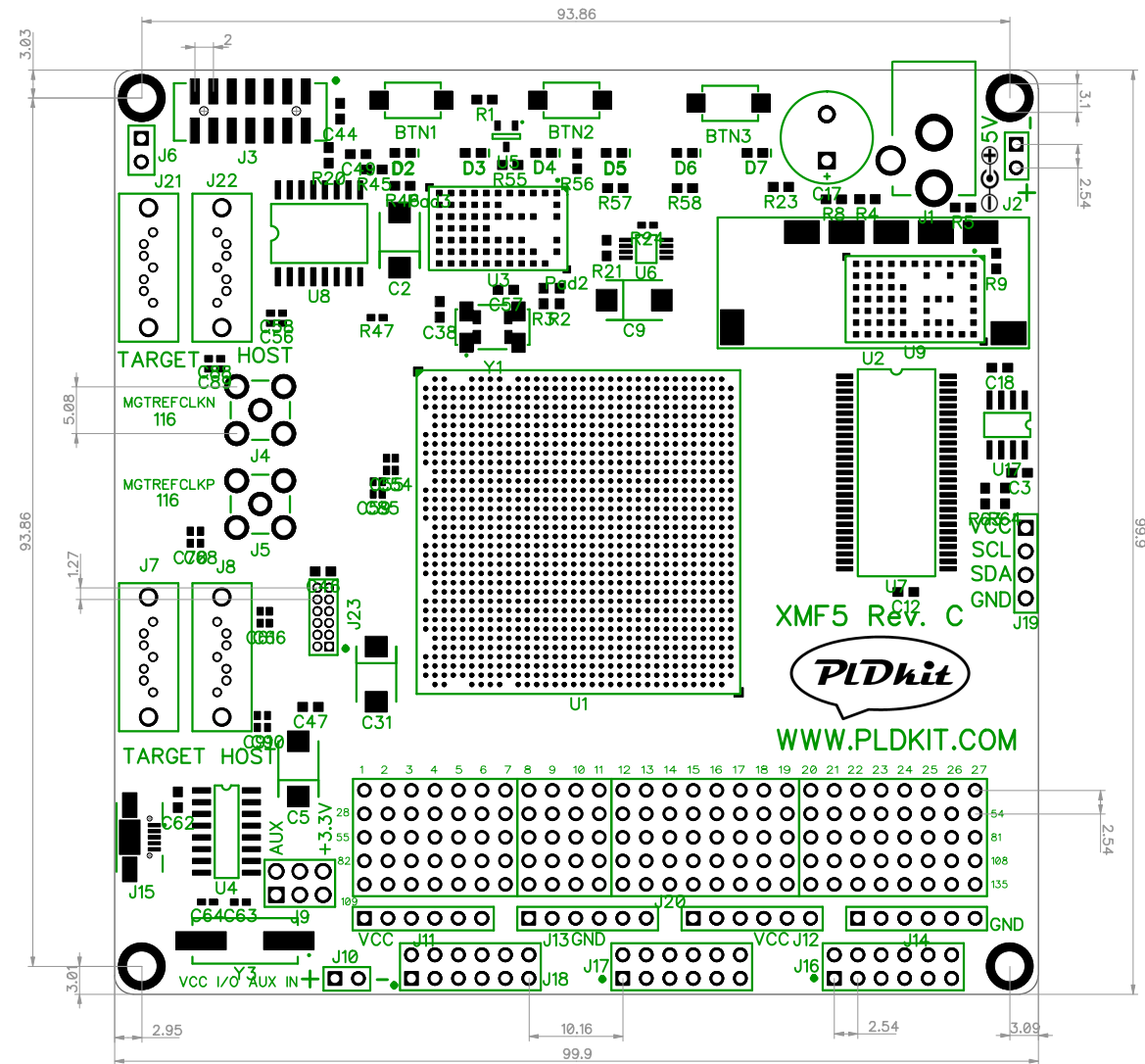






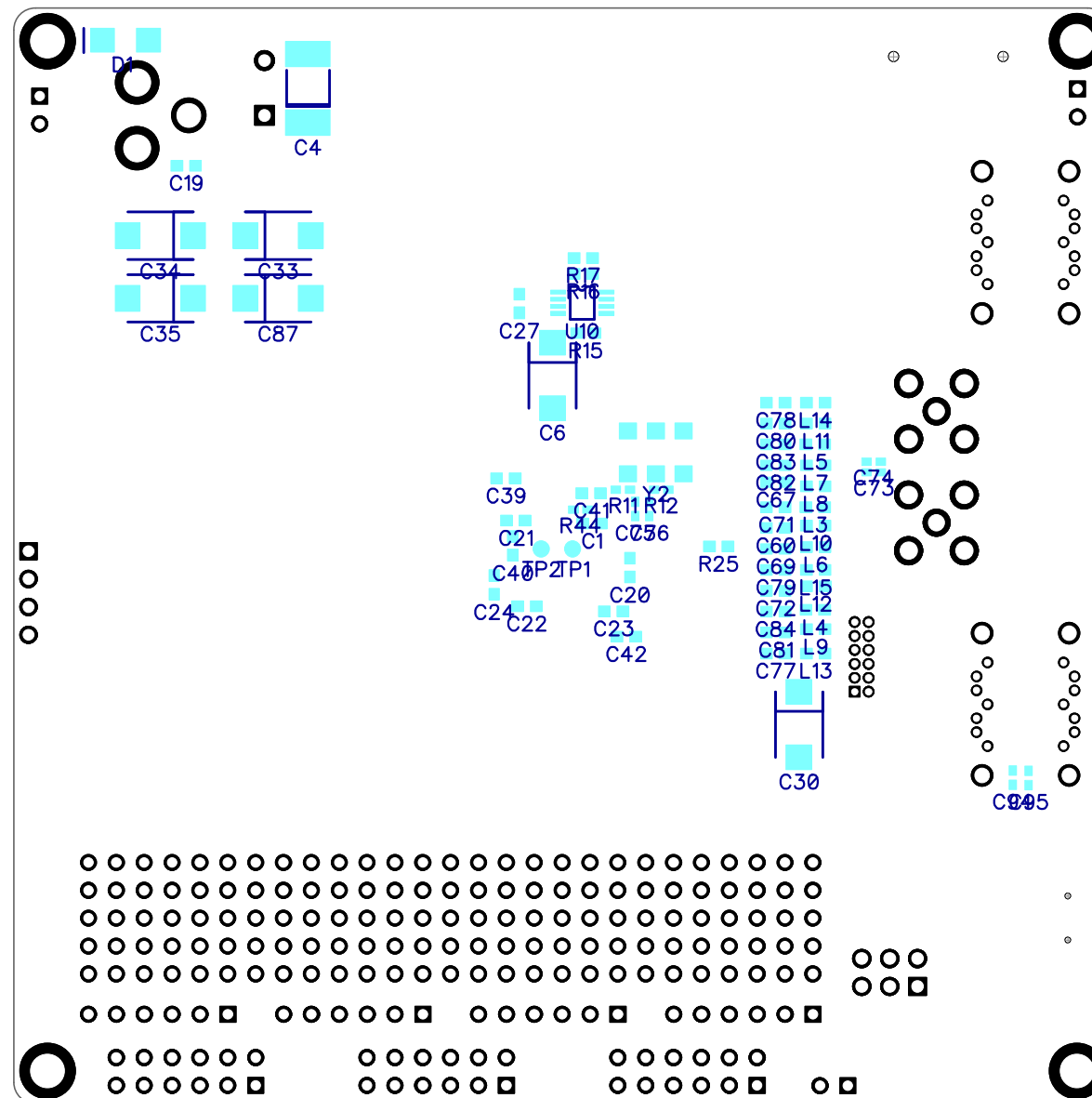


XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5-155-64-128 50/75M	Rev. C	28.09.2017	Sheet 5/5
TRANSCEIVERS			PLDkit OU www.pldkit.com



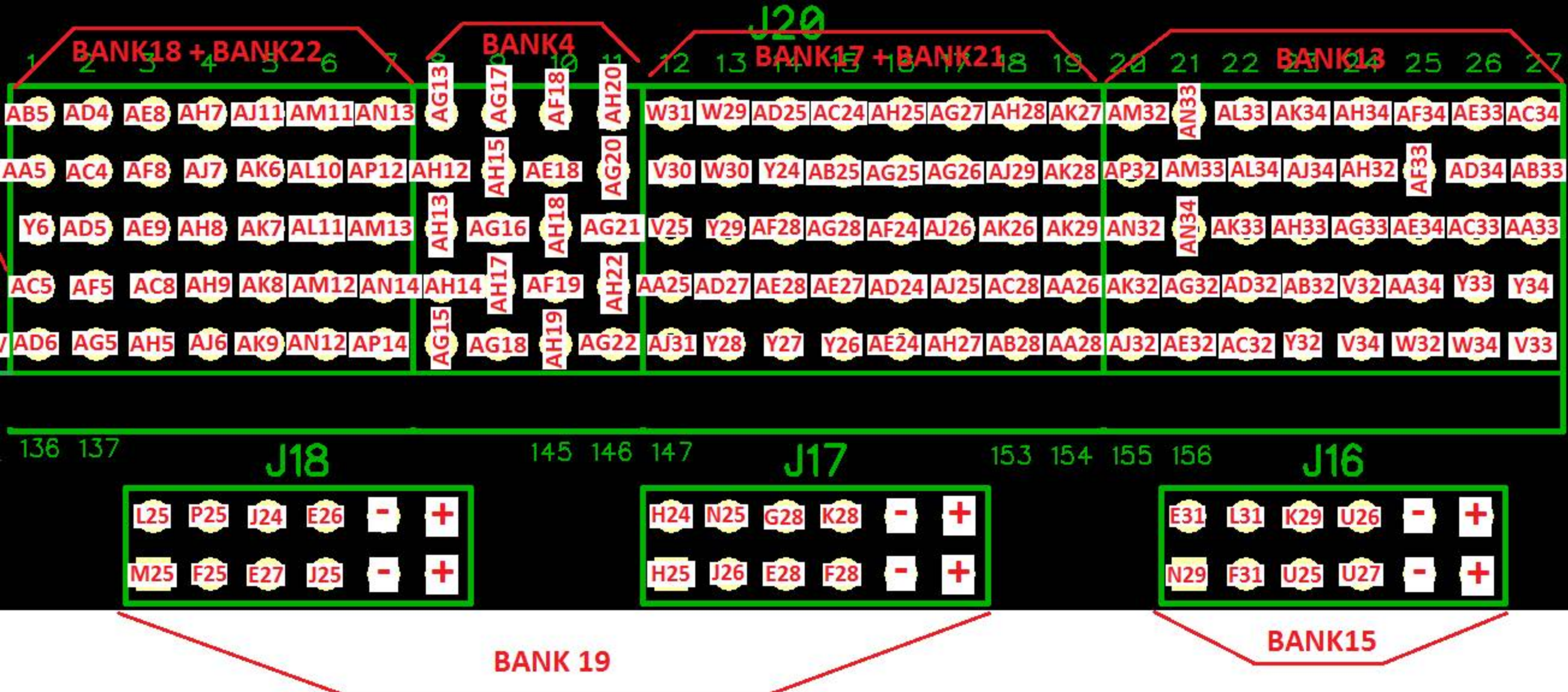
All dimensions in mm

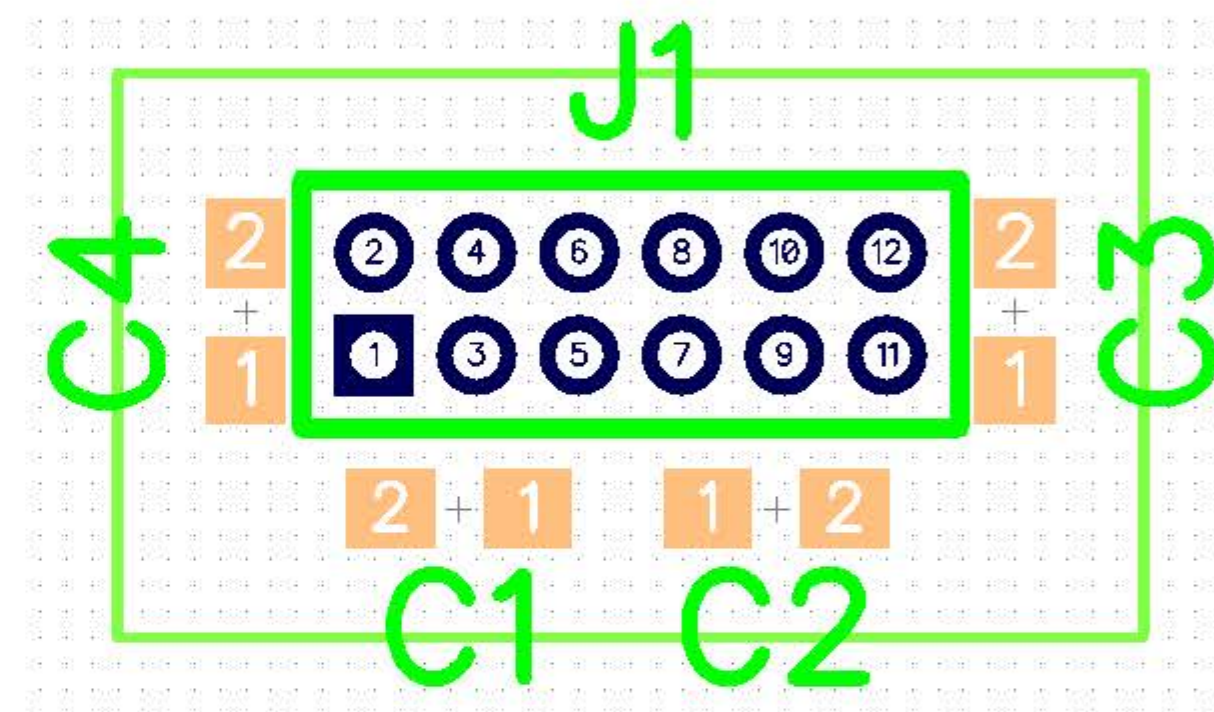
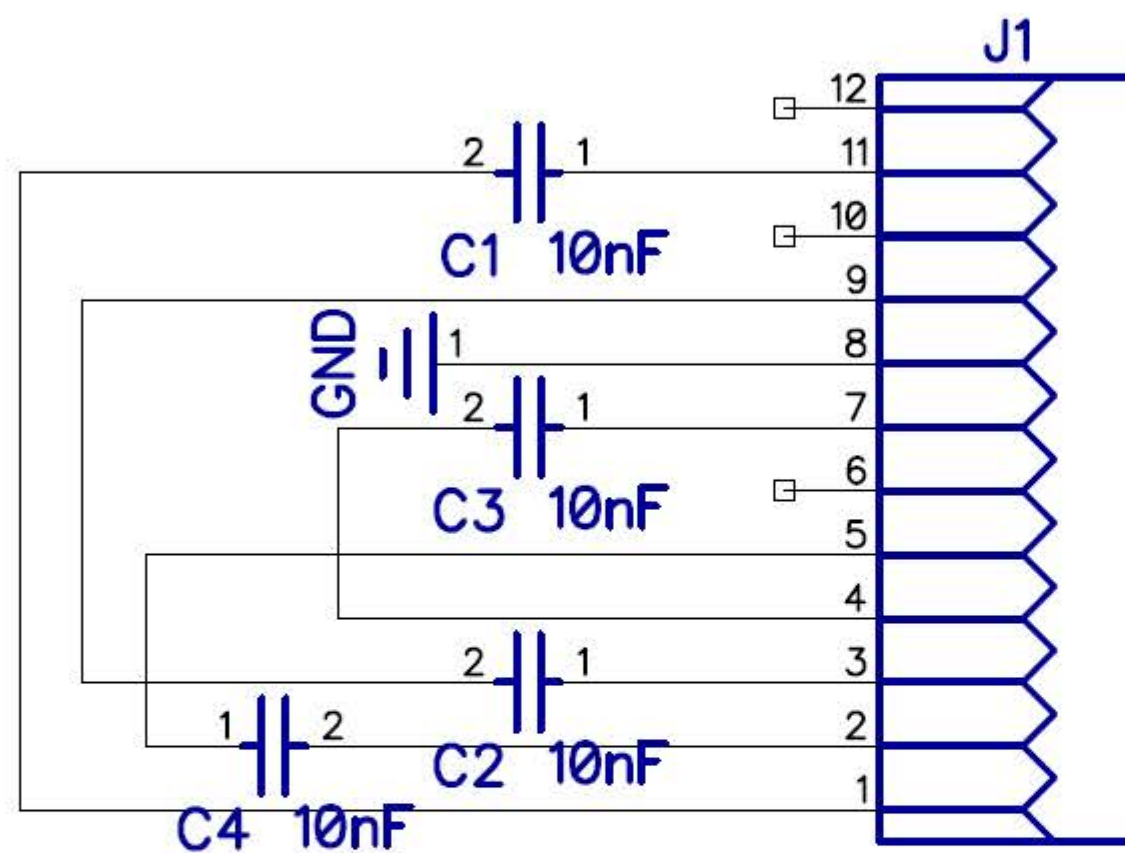
XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5	Rev. C	05.03.2016	Sheet 1/1
PCB LAYOUT			PLDkit OU www.pldkit.com



XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5	Rev. C	05.03.2016	Sheet 1/1
PCB LAYOUT			PLDkit OU www.pldkit.com







# J23 GTP LOOP TEST JUMPER FOR XMF5

Rev. A

10.01.2014

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