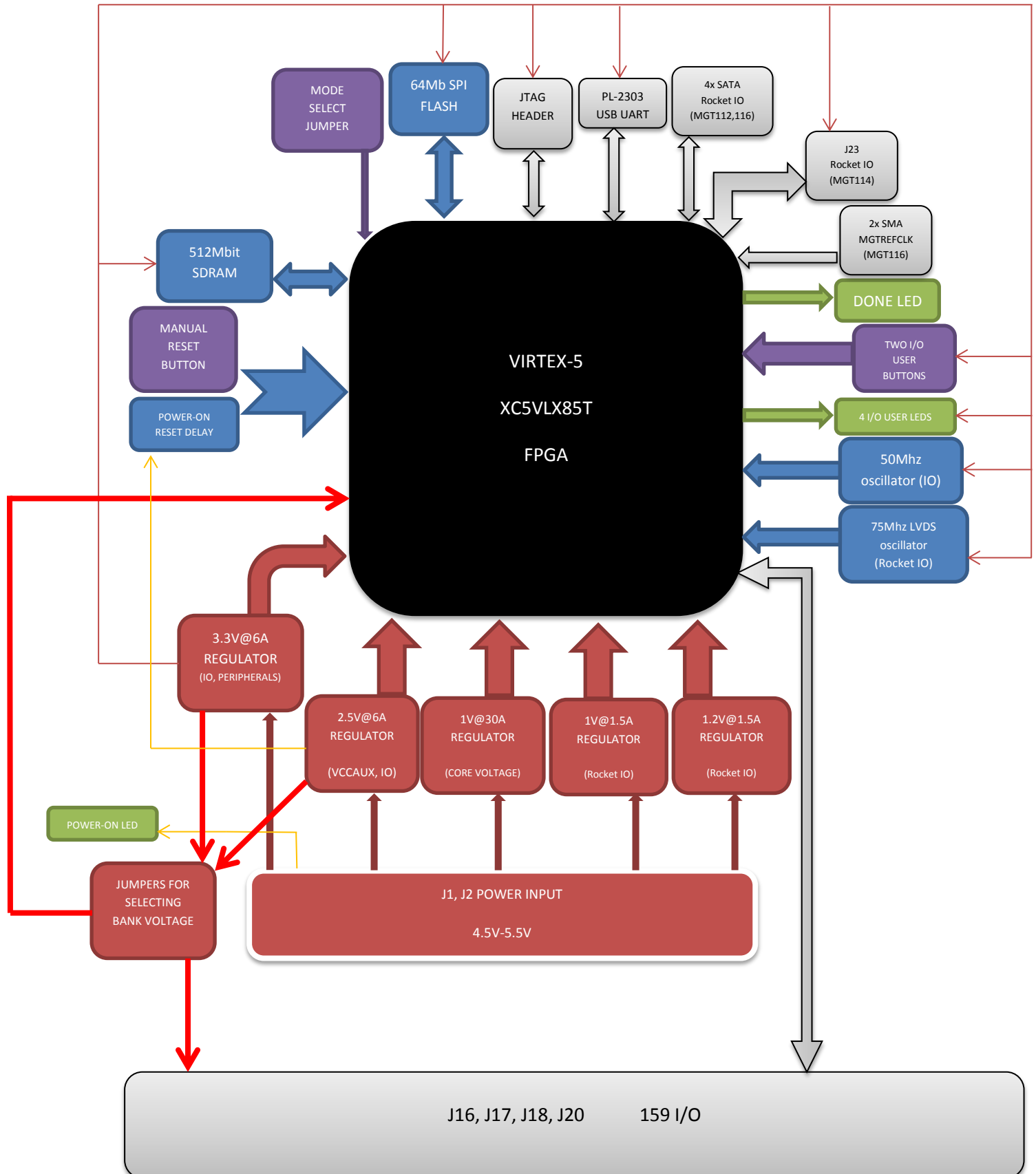
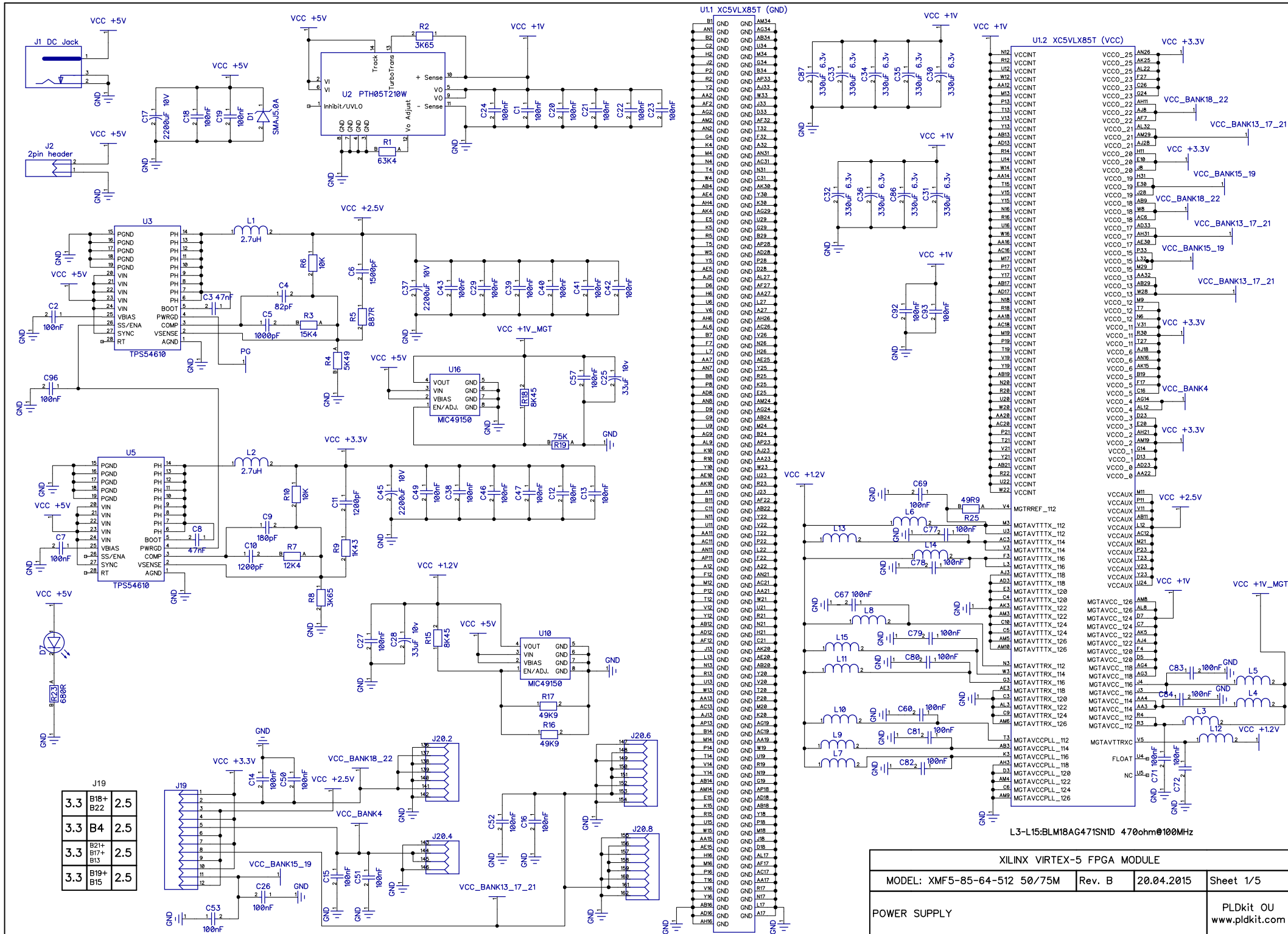
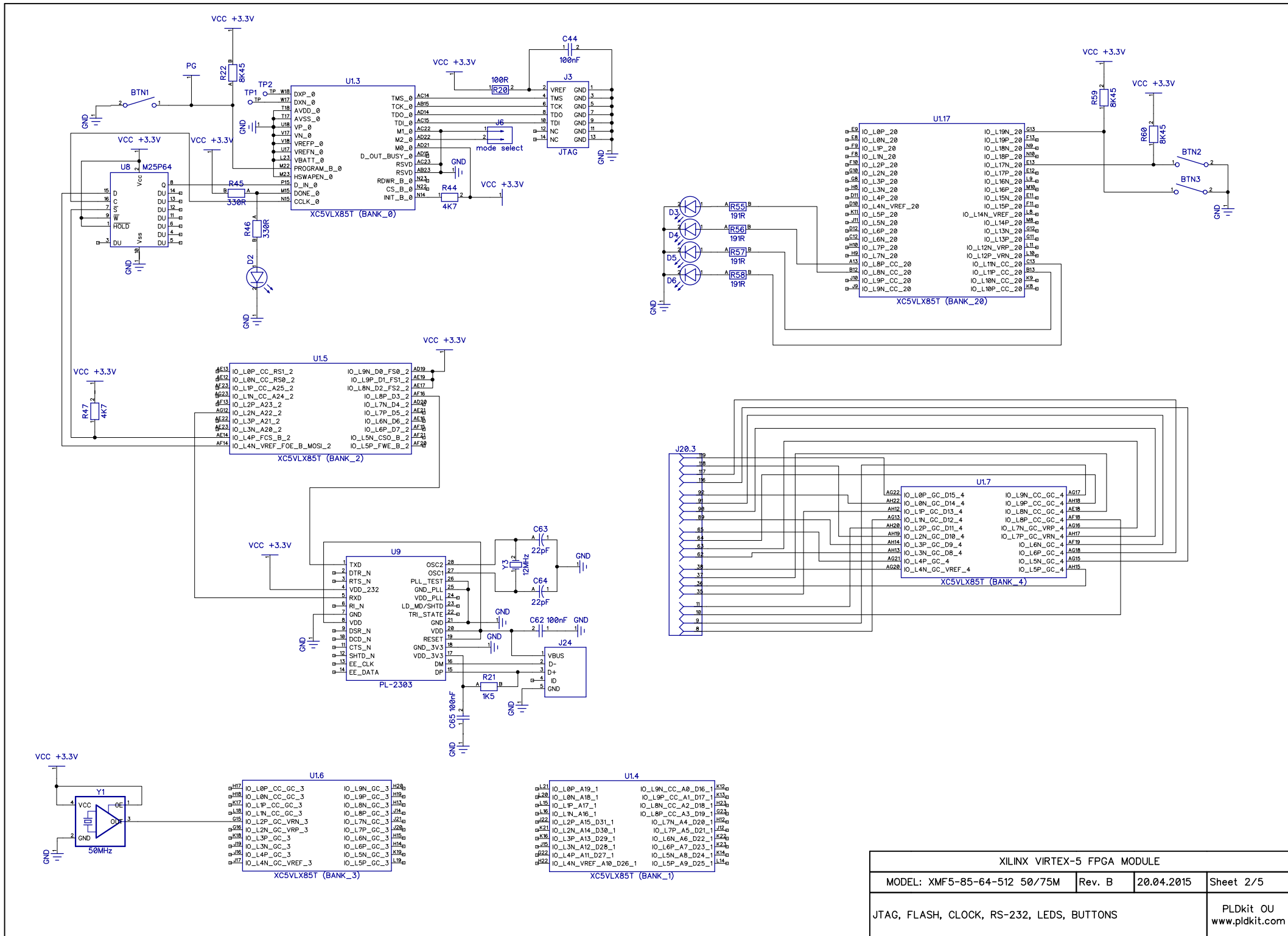


XILINX VIRTEX-5 FPGA MODULE BLOCK SCHEME

MODEL: XMF5-85-64-512 50/75M Rev.B

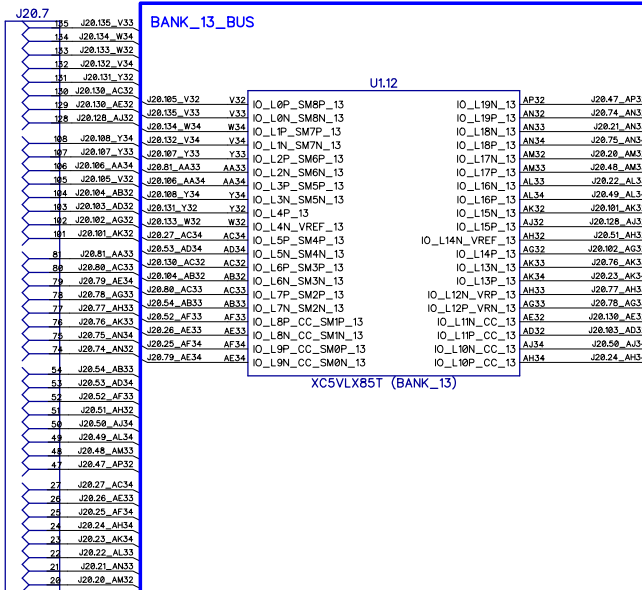
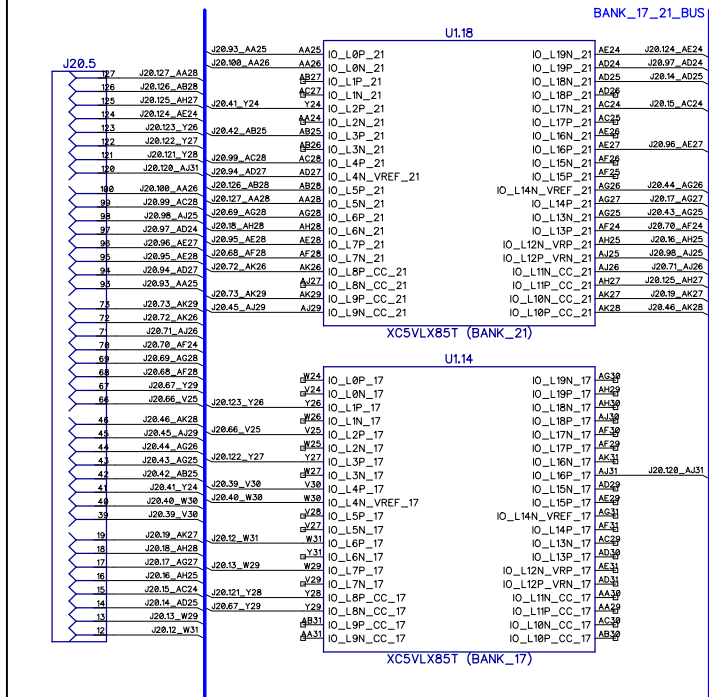
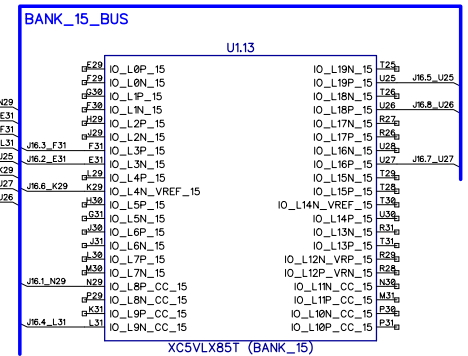
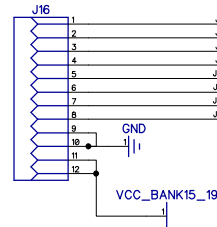
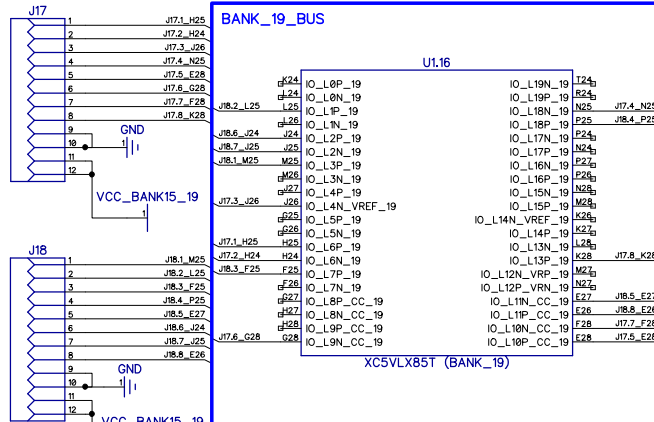
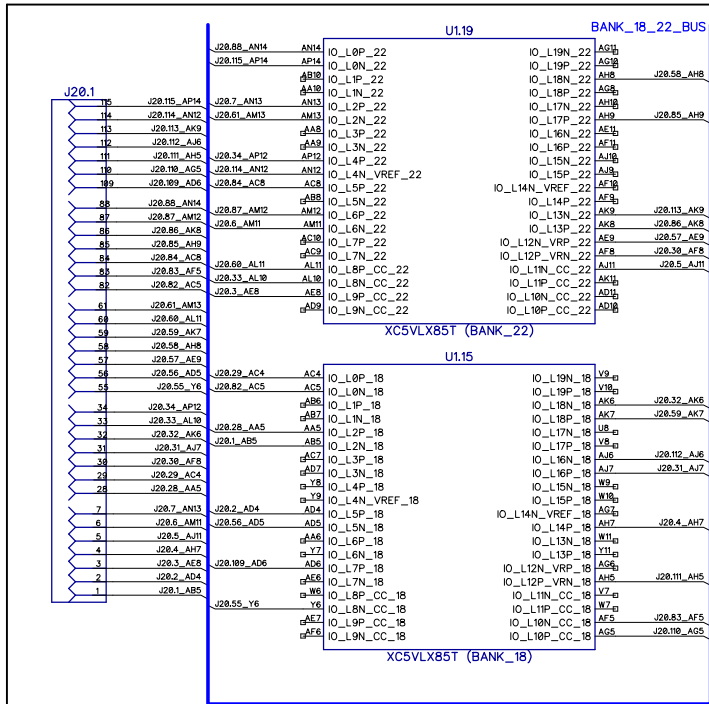




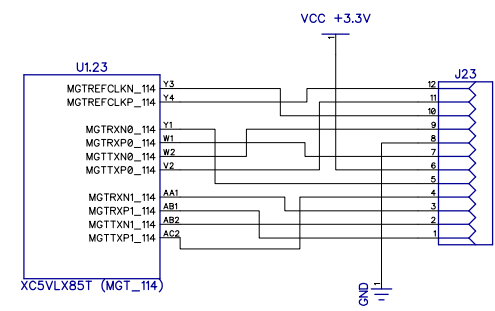
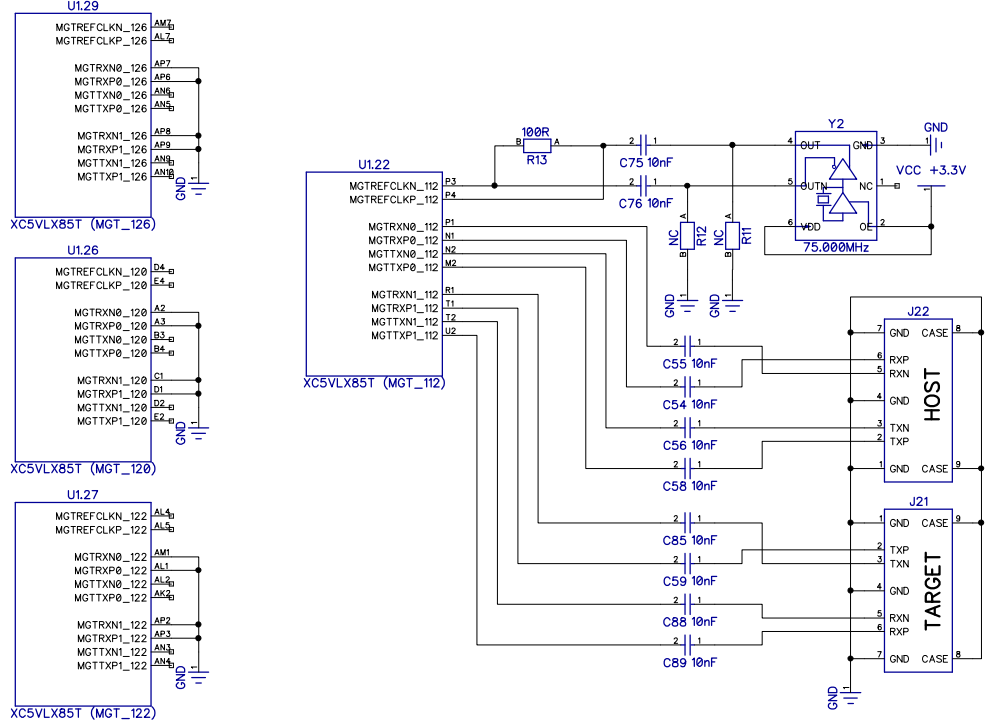
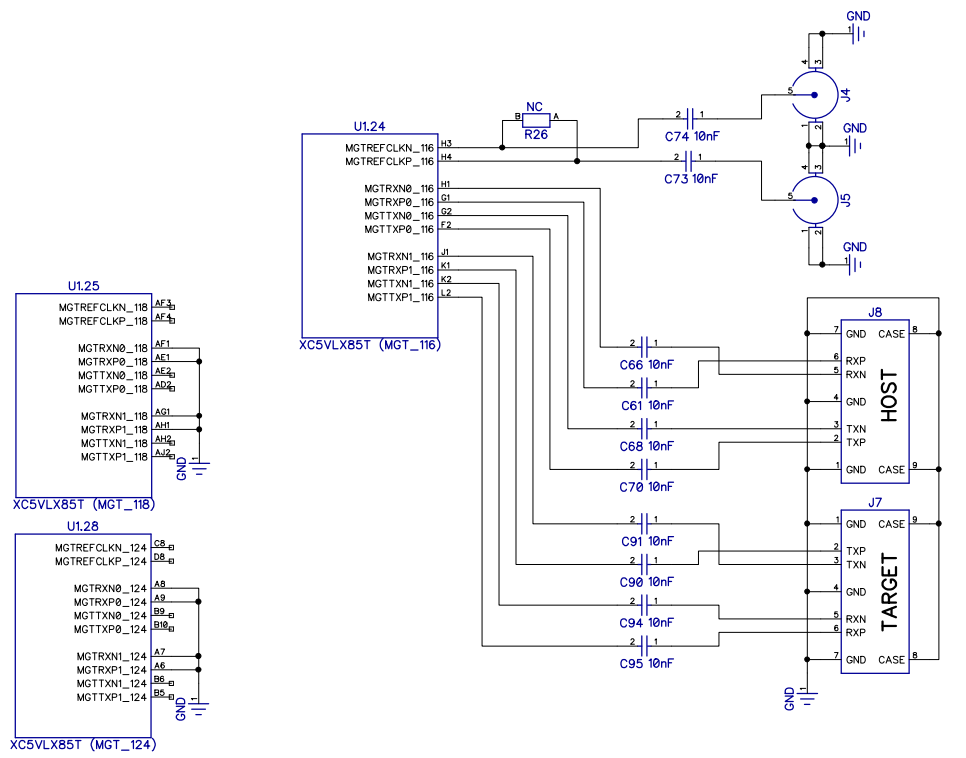


XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5-85-64-512 50/75M	Rev. B	20.04.2015	Sheet 2/5
JTAG, FLASH, CLOCK, RS-232, LEDS, BUTTONS			PLDkit OU www.pldkit.com

XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5-85-64-512 50/75M	Rev. B	20.04.2015	Sheet 3/5
RAM			PLDkit OU www.pldkit.com

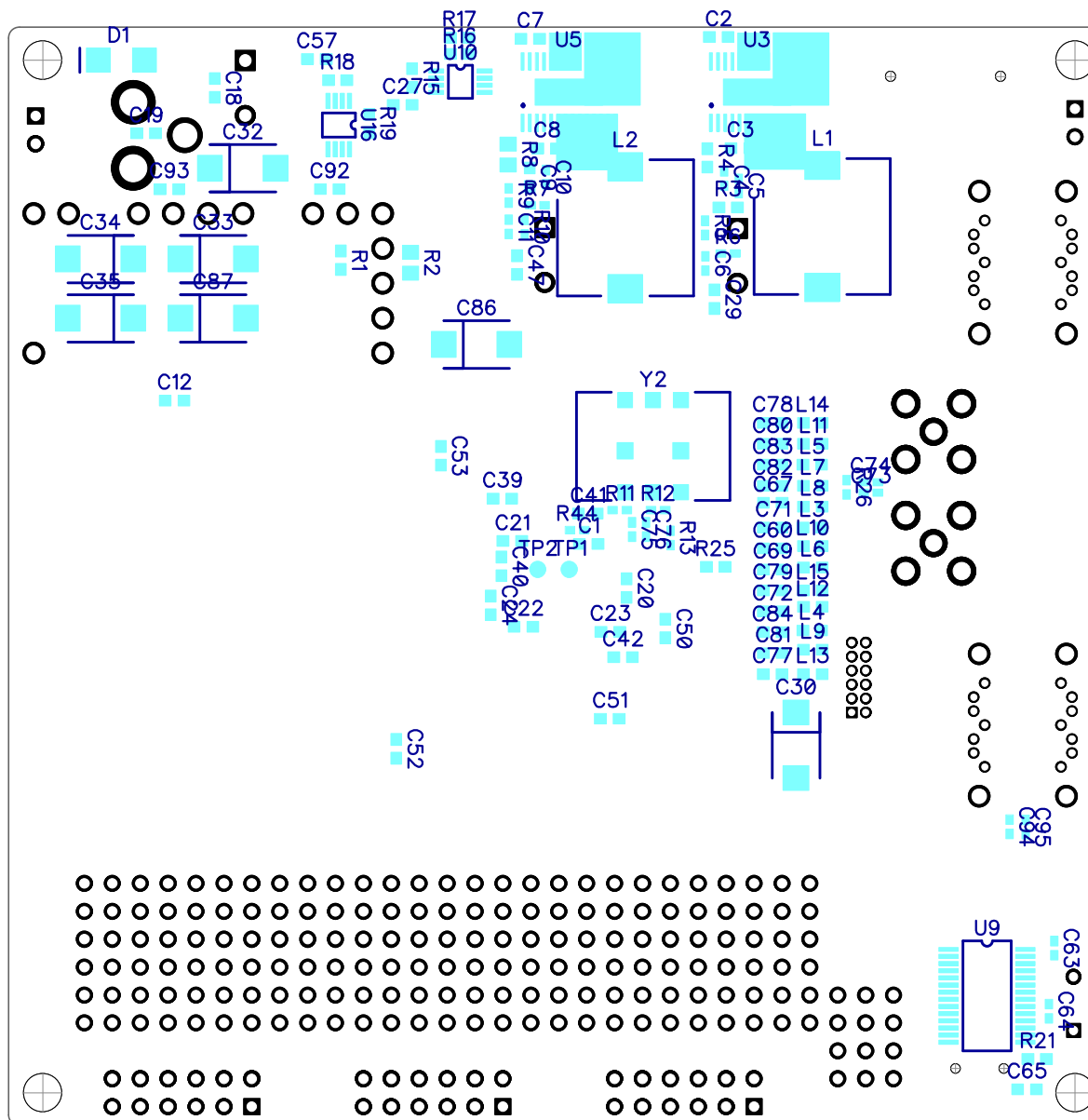


XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5-85-64-512 50/75M	Rev. B	20.04.2015	Sheet 4/5
IO CONNECTORS			PLDkit OU www.pldkit.com

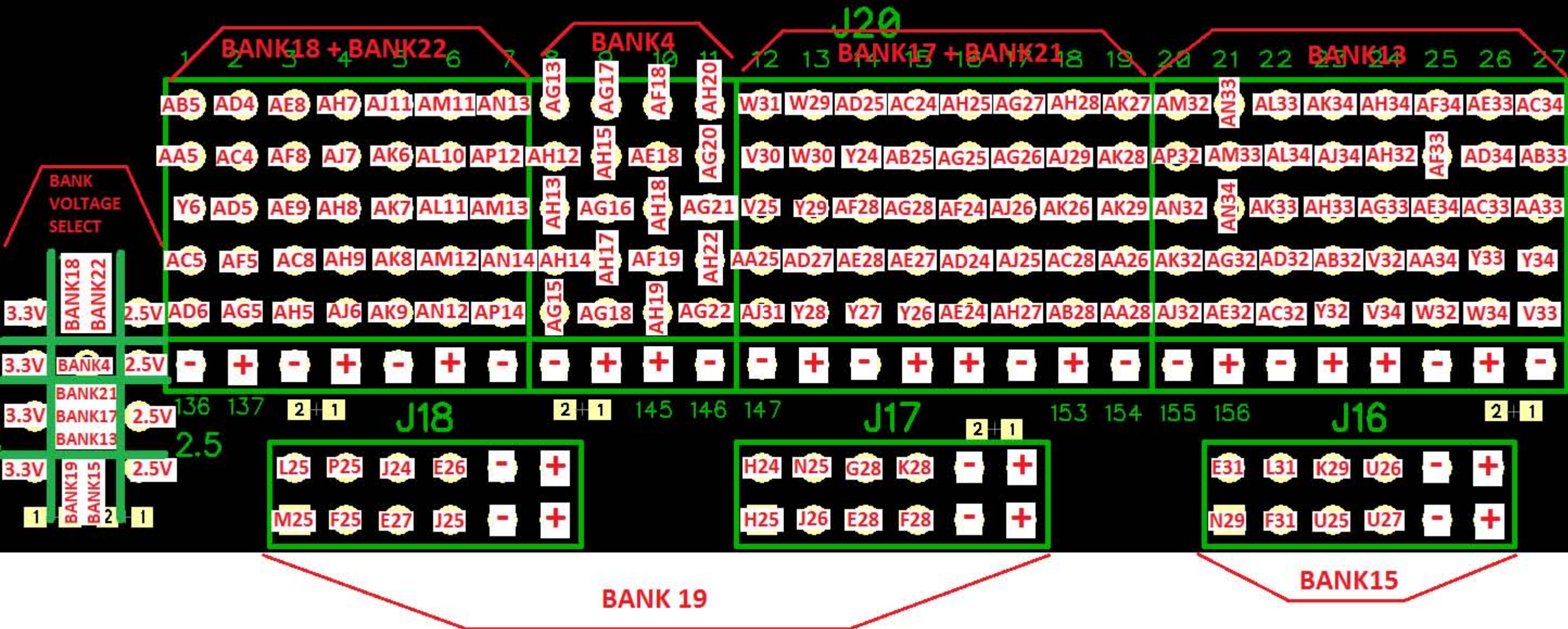


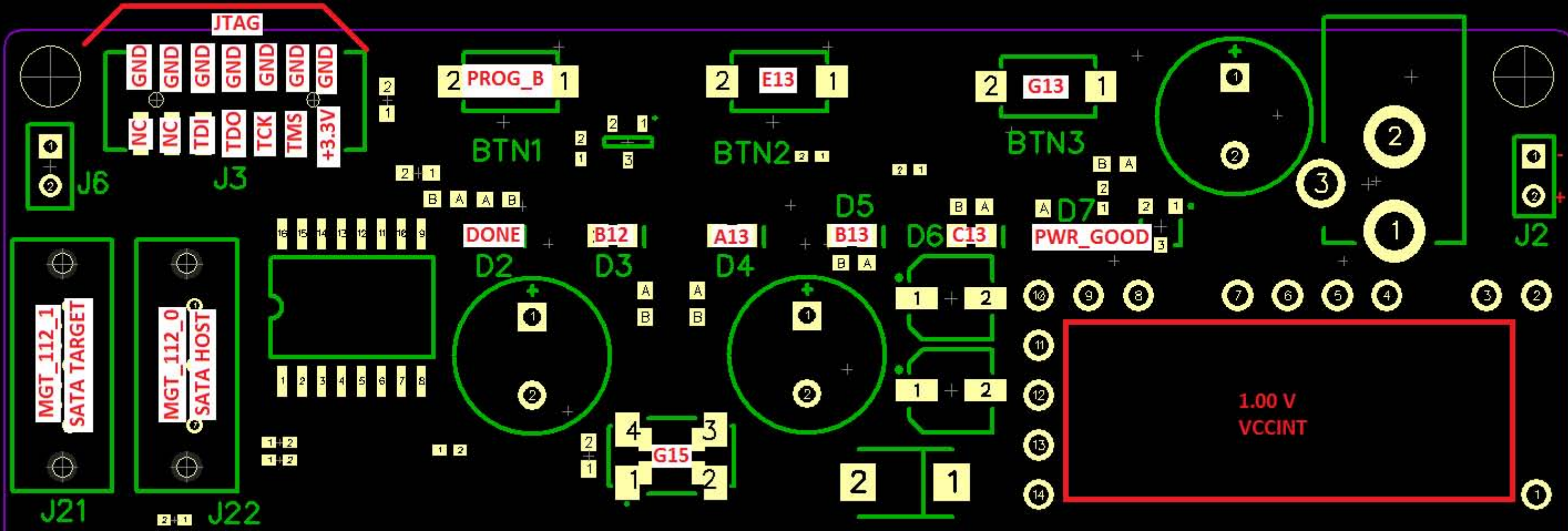
XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5-85-64-512 50/75M	Rev. B	20.04.2015	Sheet 5/5
TRANSCIEVERS			PLDkit OU www.pldkit.com

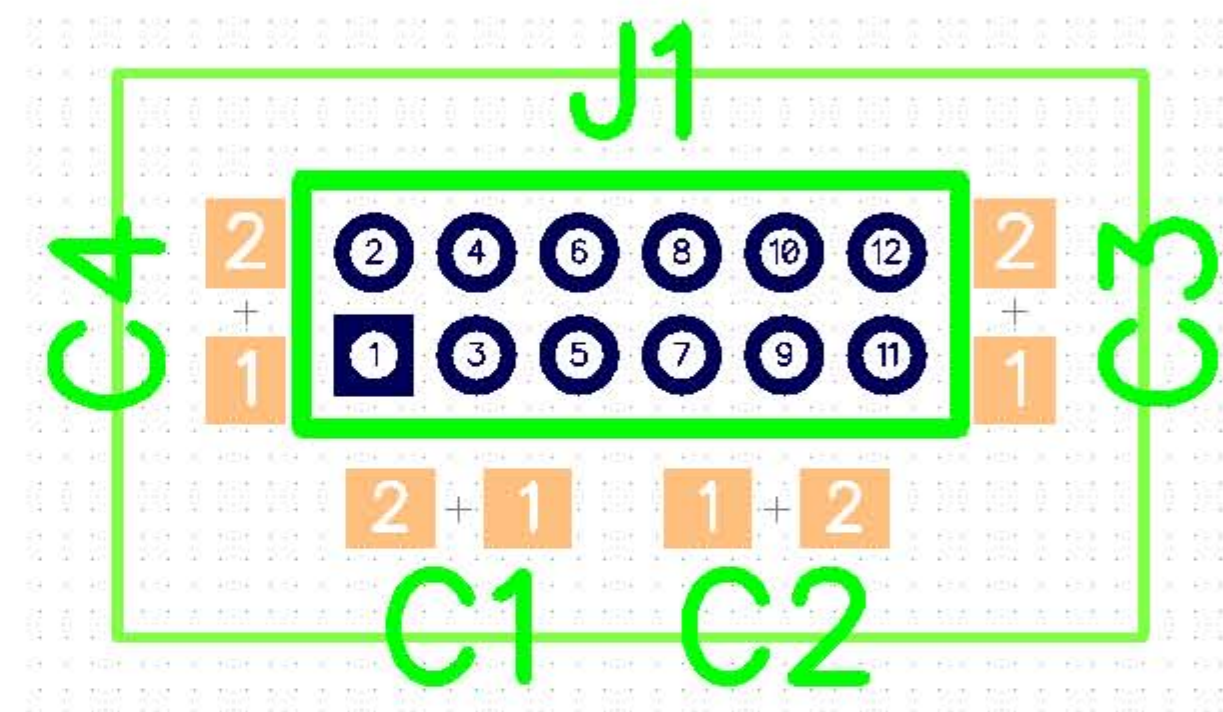
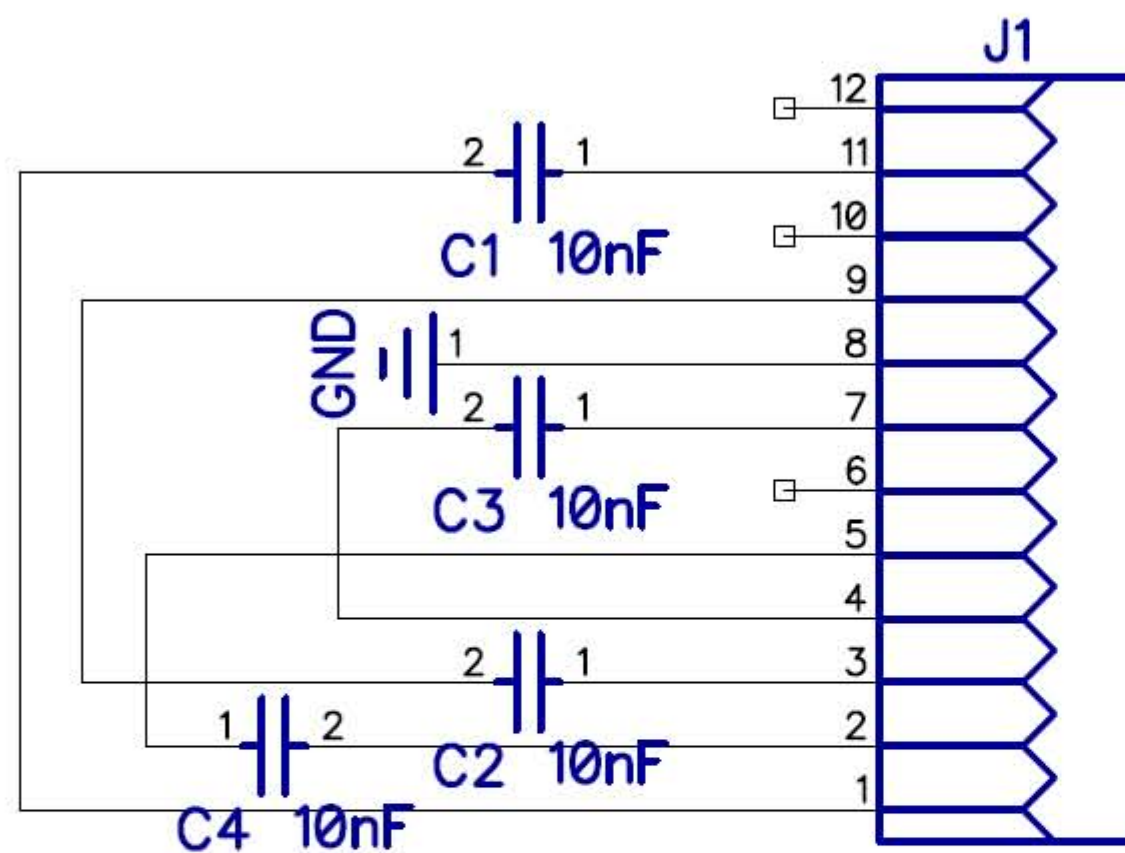




XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5	Rev. B	19.04.2015	Sheet 1/1
PCB LAYOUT			PLDkit OU www.pldkit.com







J23 GTP LOOP TEST JUMPER FOR XMF5

Rev. A

10.01.2014

Sheet 1/1