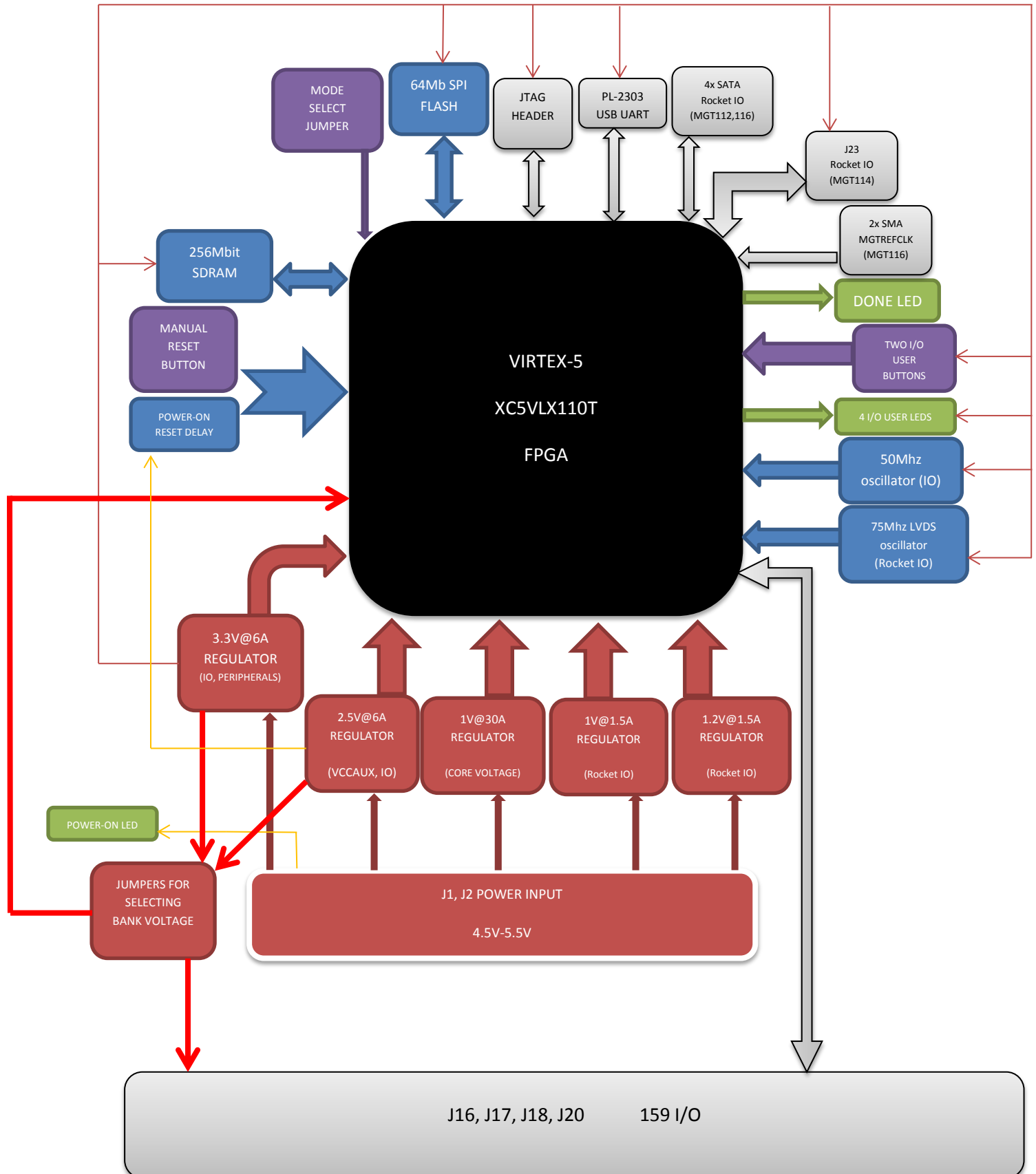
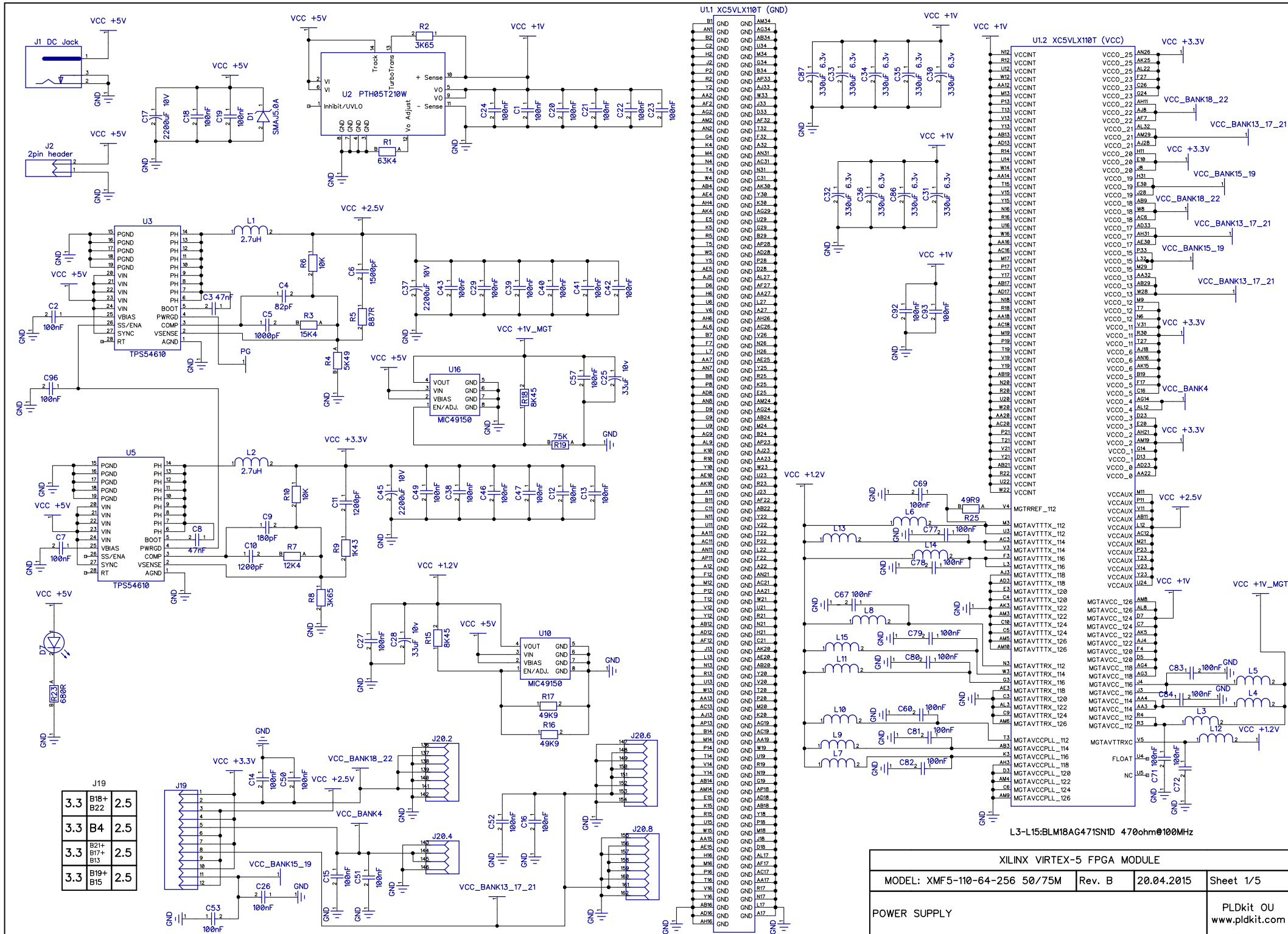


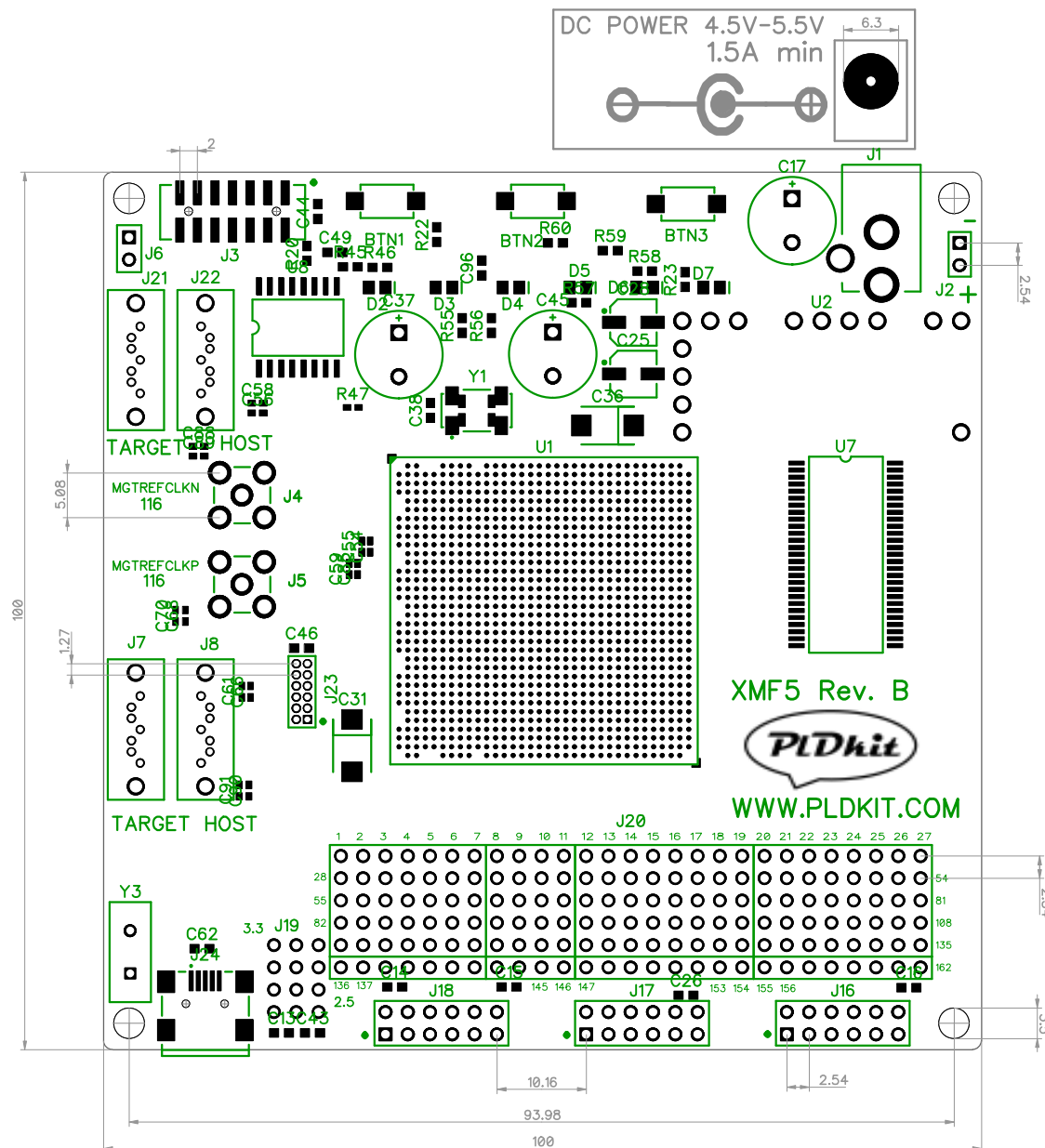
XILINX VIRTEX-5 FPGA MODULE BLOCK SCHEME

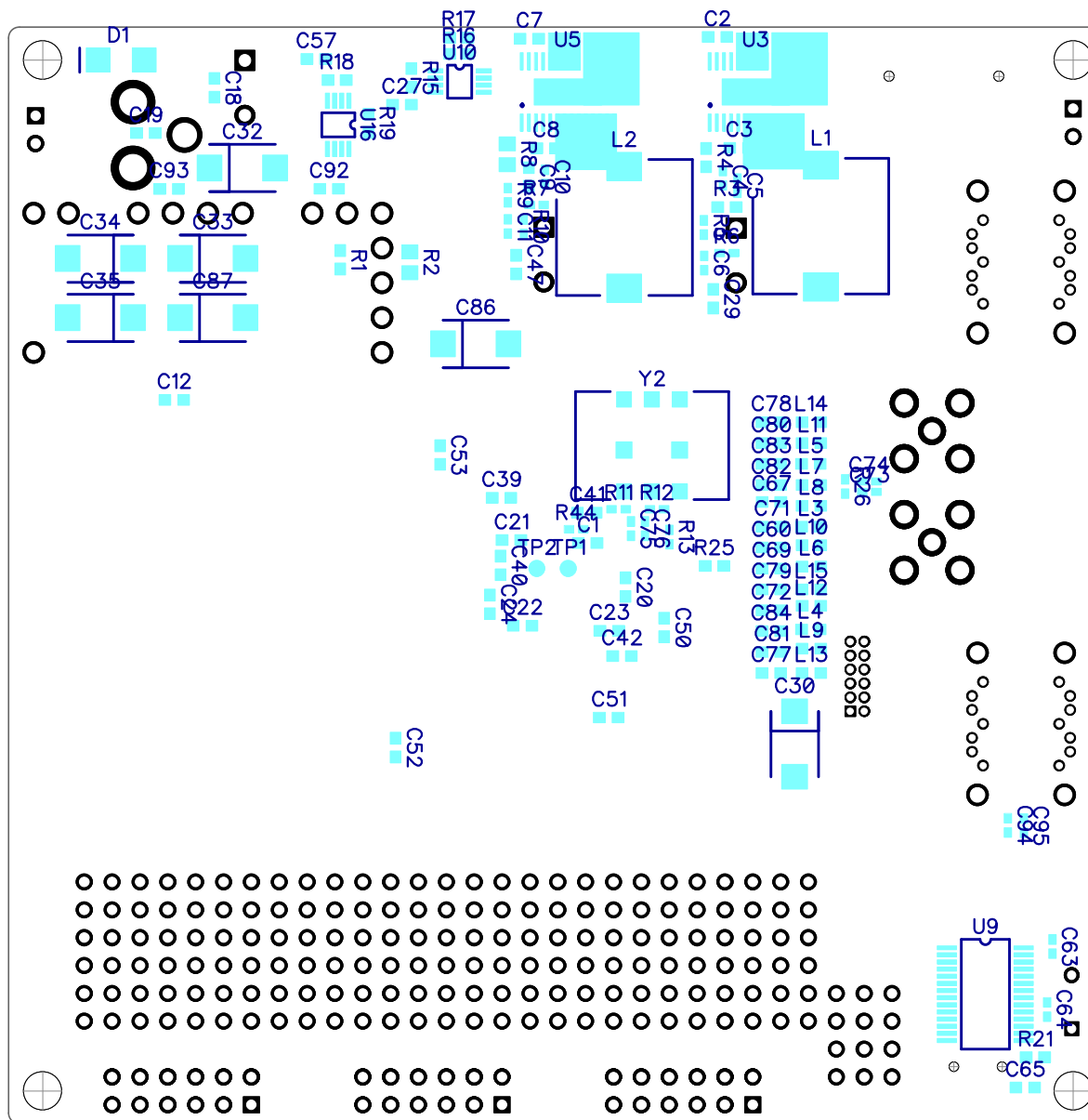
MODEL: XMF5-110-64-256 50/75M Rev.B



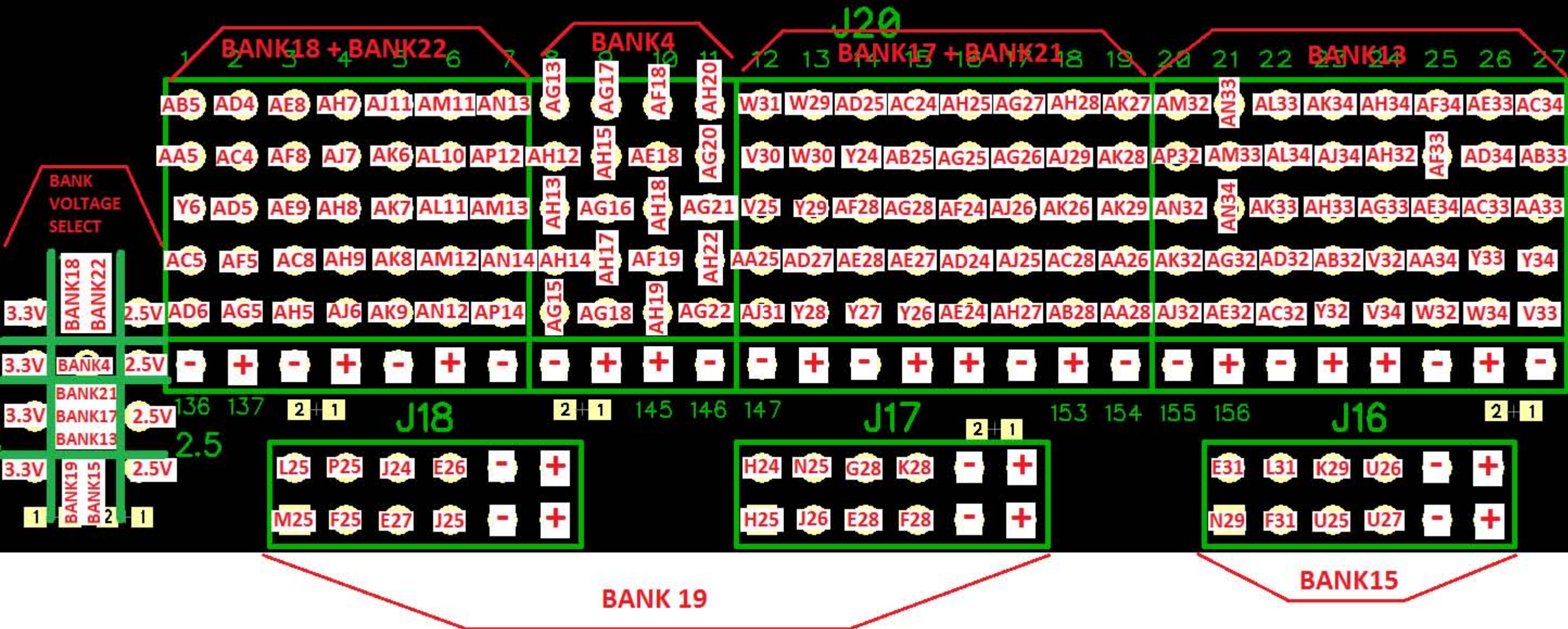


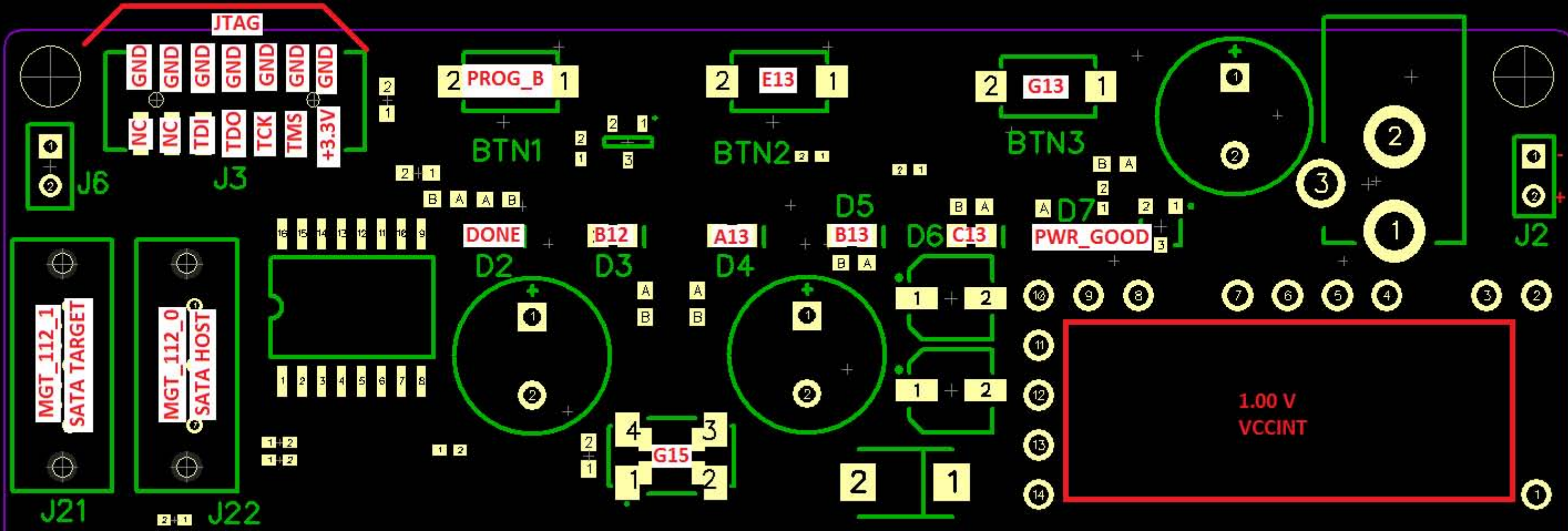
XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5-110-64-256 50/75M	Rev. B	20.04.2015	Sheet 3/5
RAM			PLDkit OU www.pldkit.com

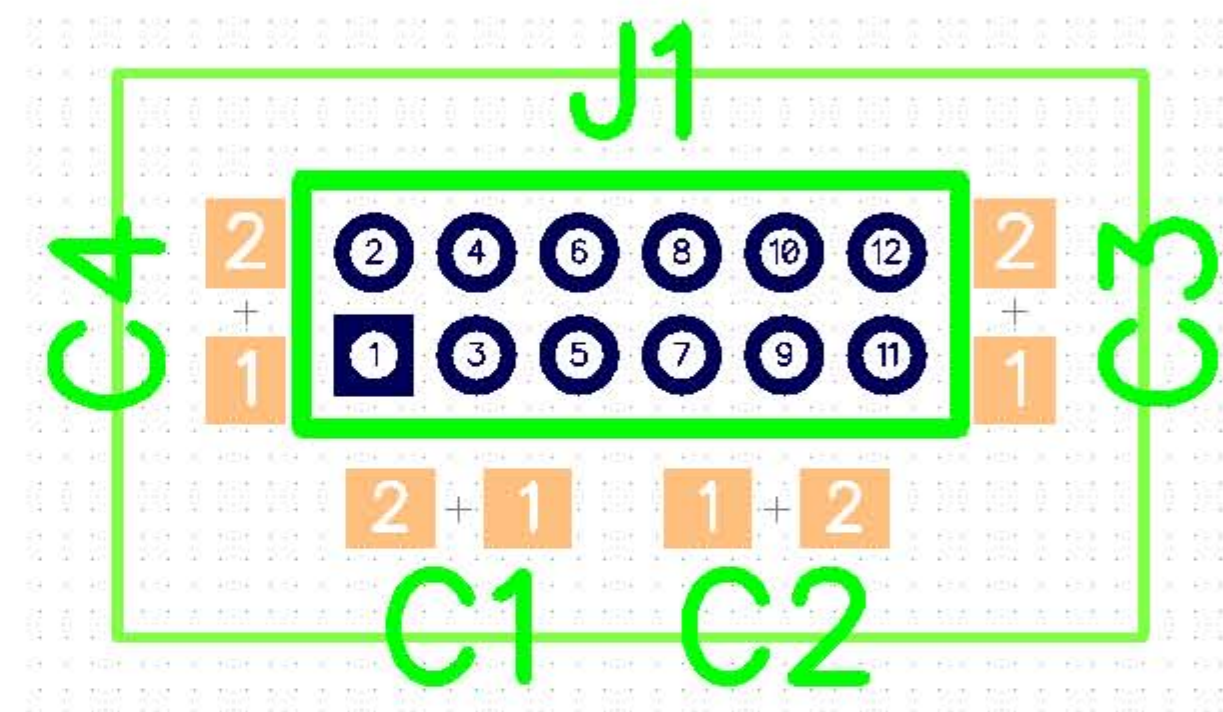
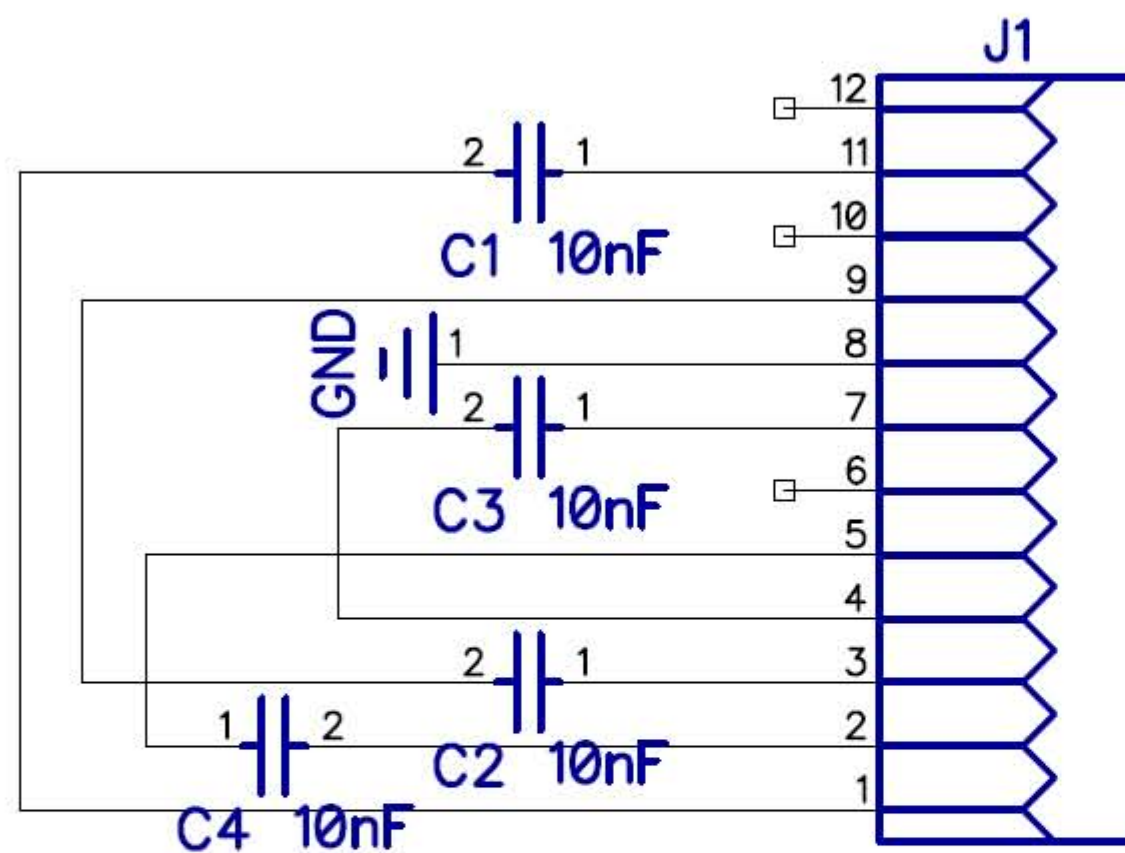




XILINX VIRTEX-5 FPGA MODULE			
MODEL: XMF5	Rev. B	19.04.2015	Sheet 1/1
PCB LAYOUT			PLDkit OU www.pldkit.com







J23 GTP LOOP TEST JUMPER FOR XMF5

Rev. A

10.01.2014

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